

FIG. 1

2001-20b.vsd/2

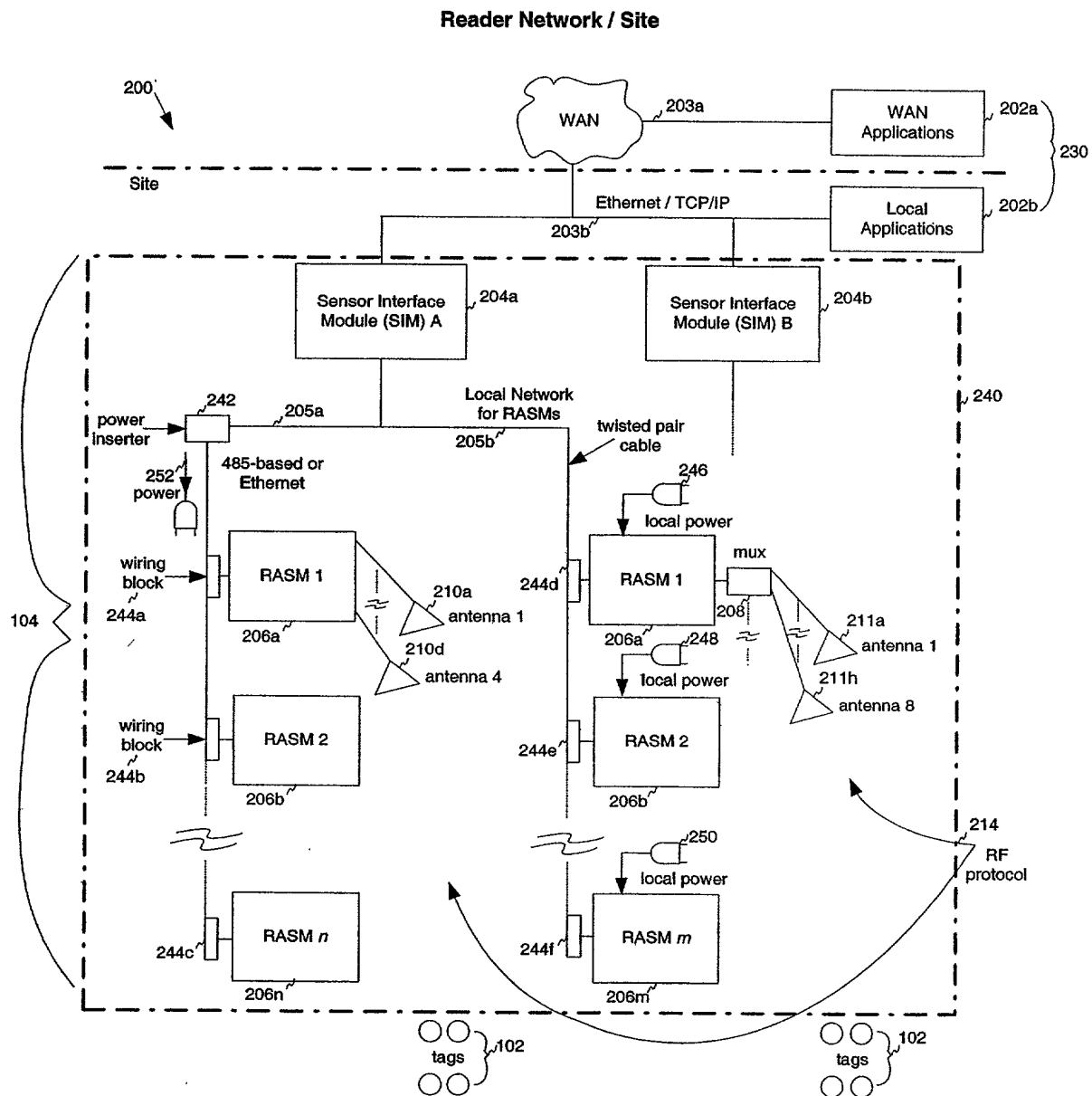


FIG. 2

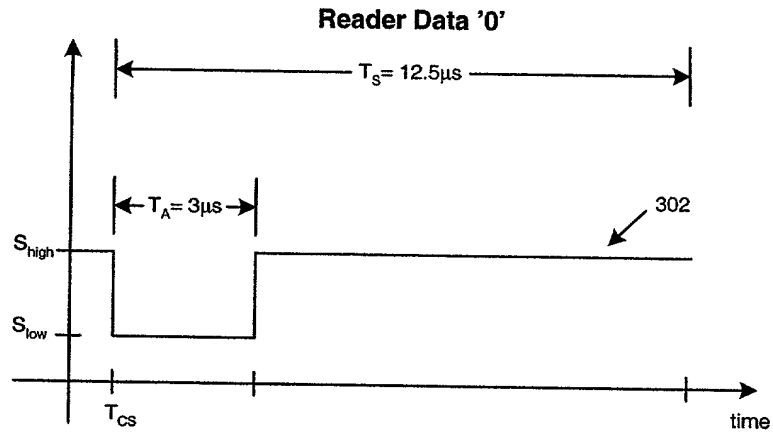


FIG. 3

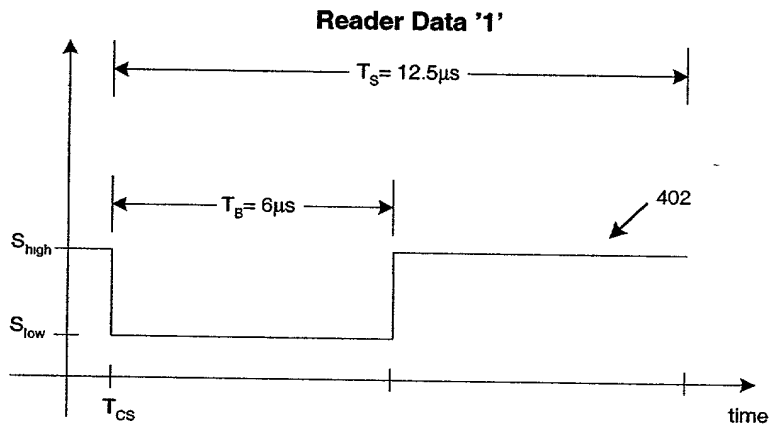


FIG. 4

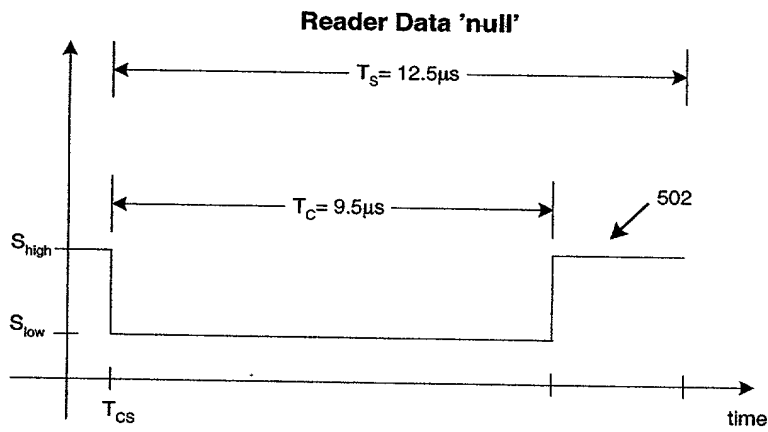


FIG. 5

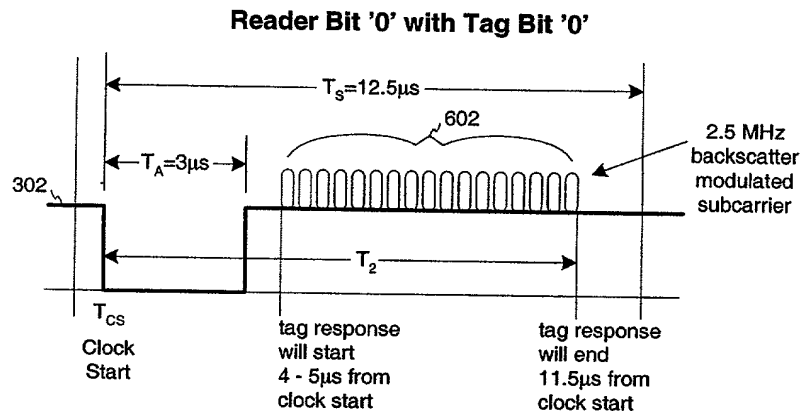


FIG. 6

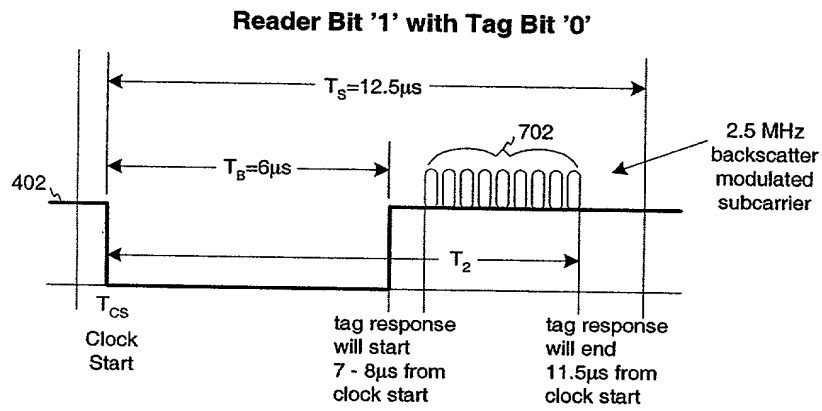


FIG. 7

Reader Bit '0' with Tag Bit '1'

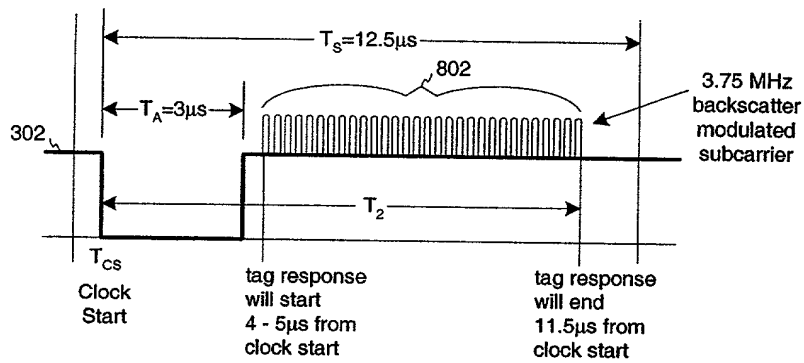


FIG. 8

Reader Bit '1' with Tag Bit '1'

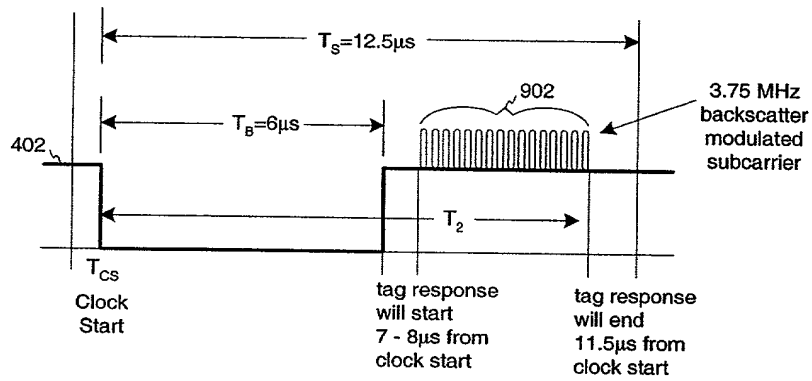


FIG. 9

20072934-0100

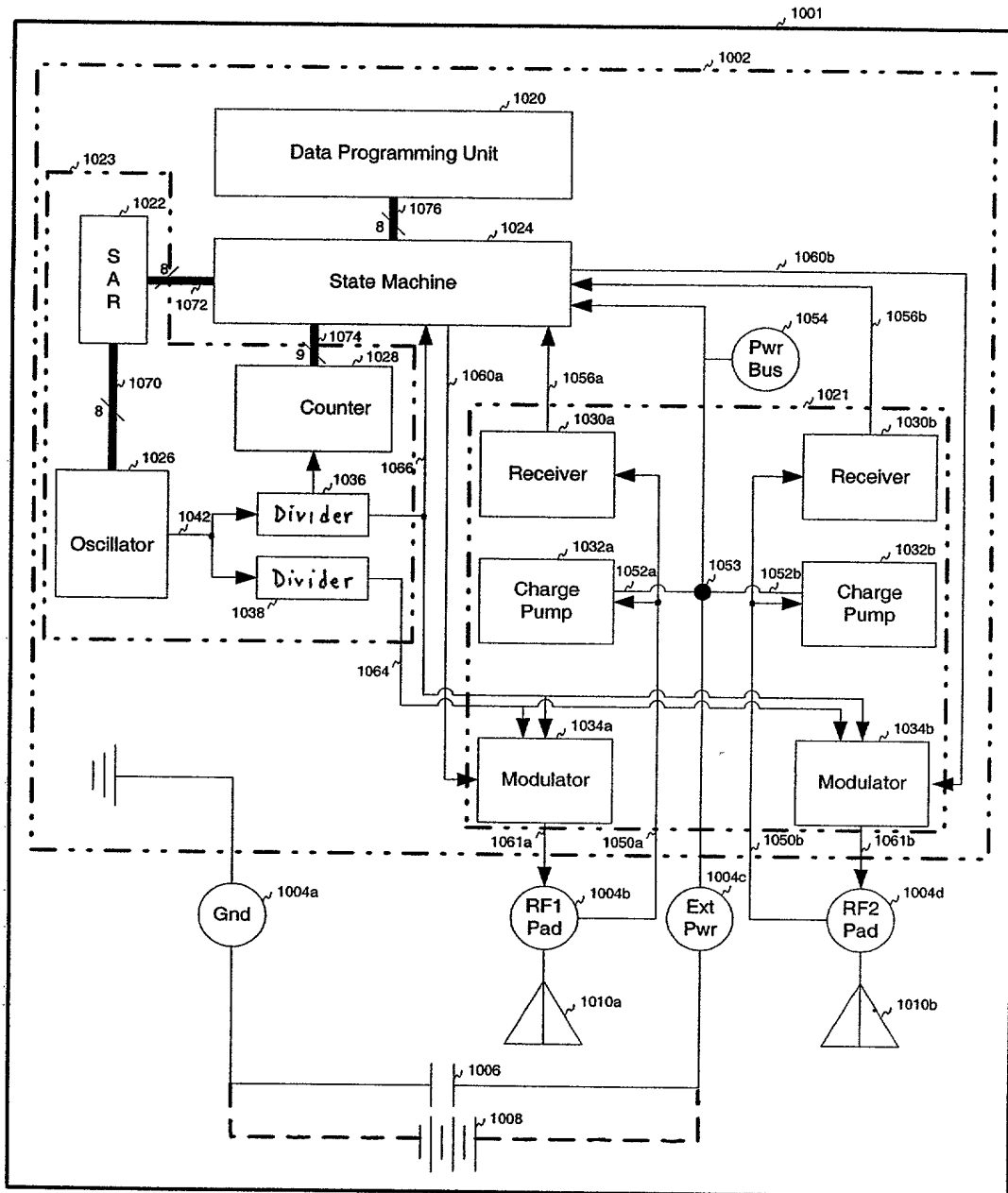


FIG. 10

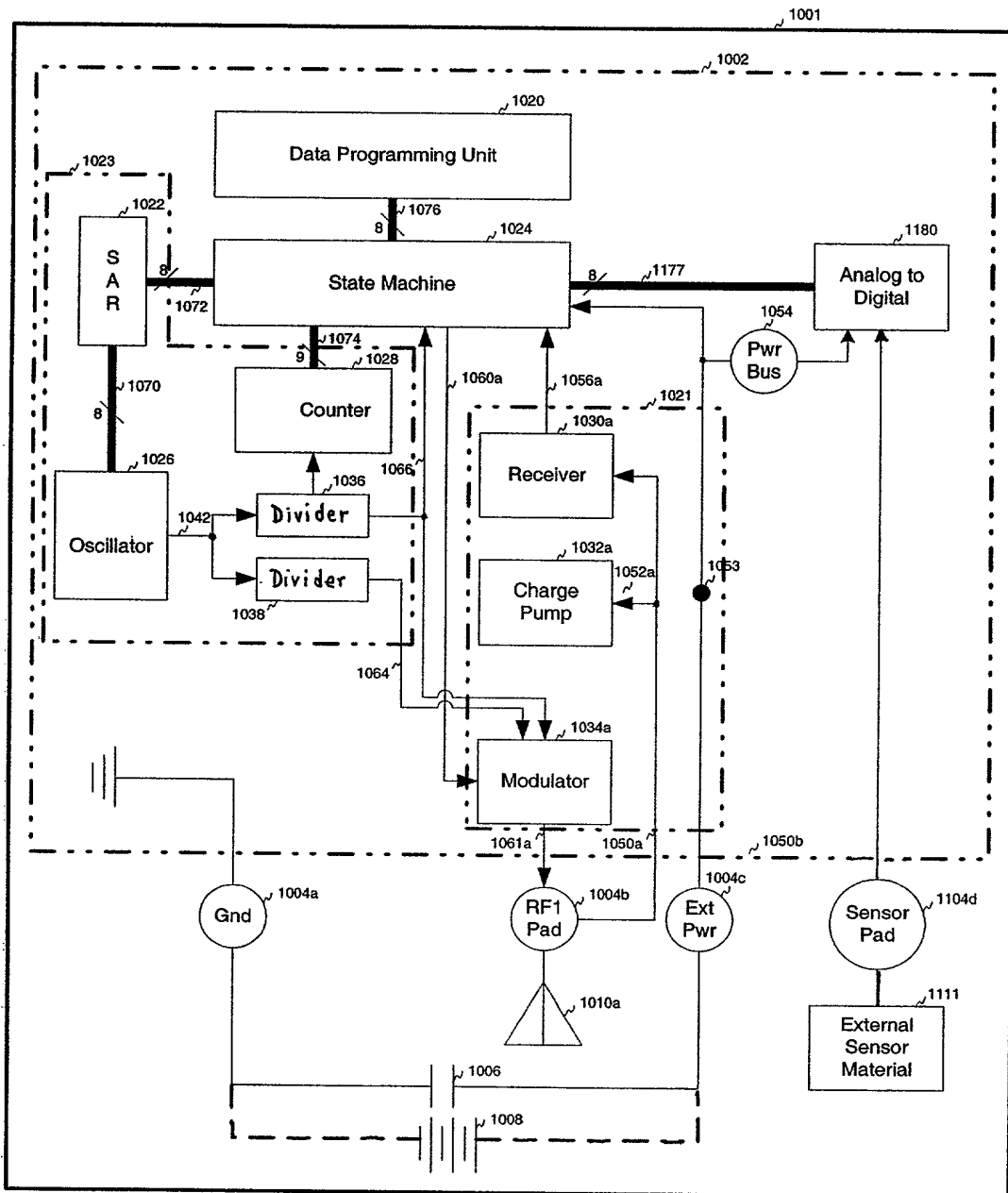


FIG. 11

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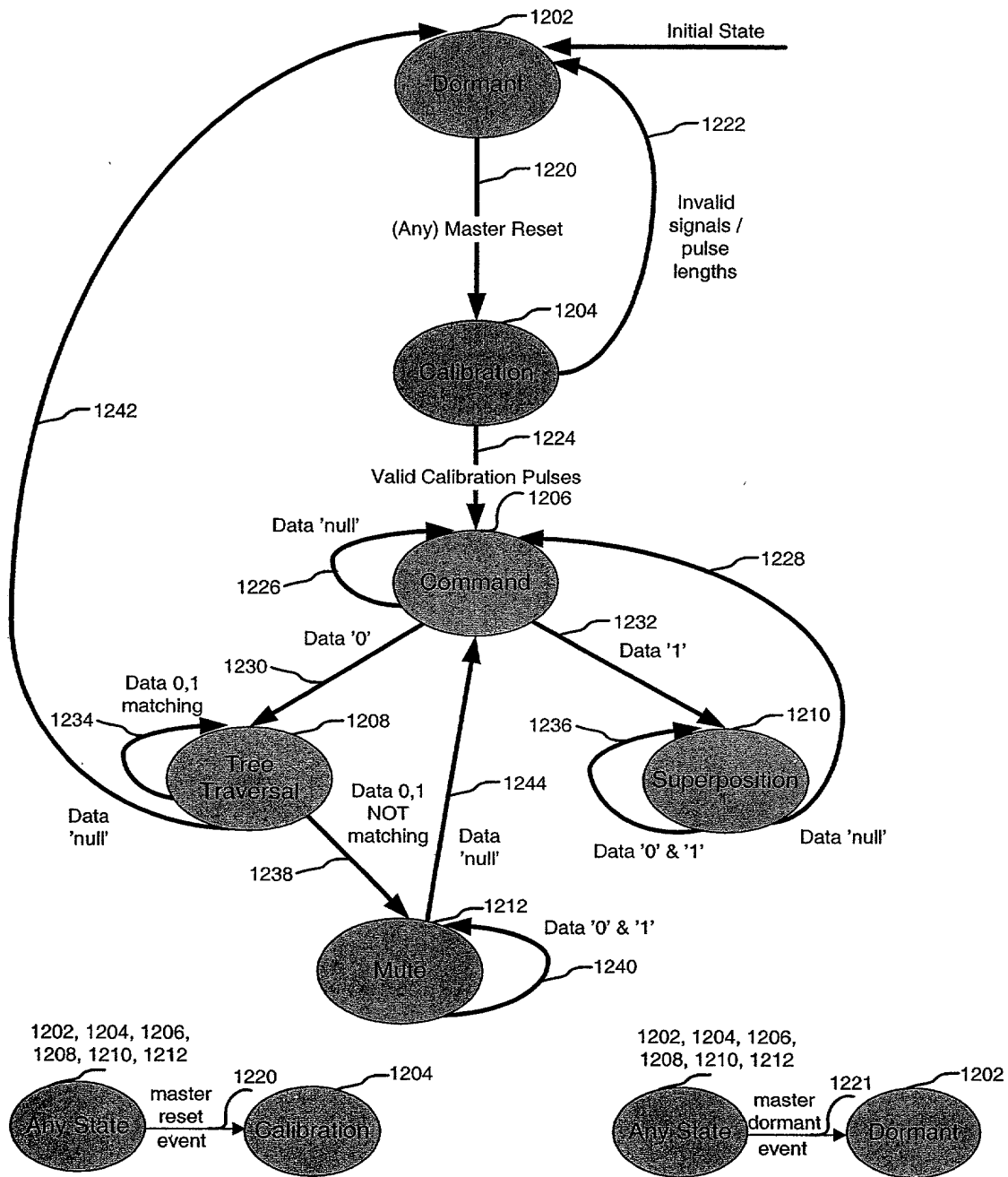


FIG. 12A

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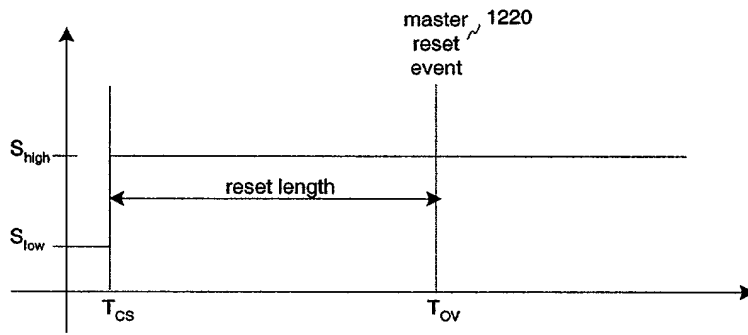


FIG. 12B

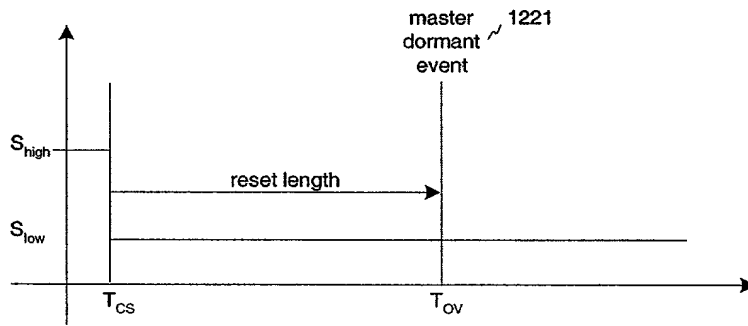


FIG. 12C

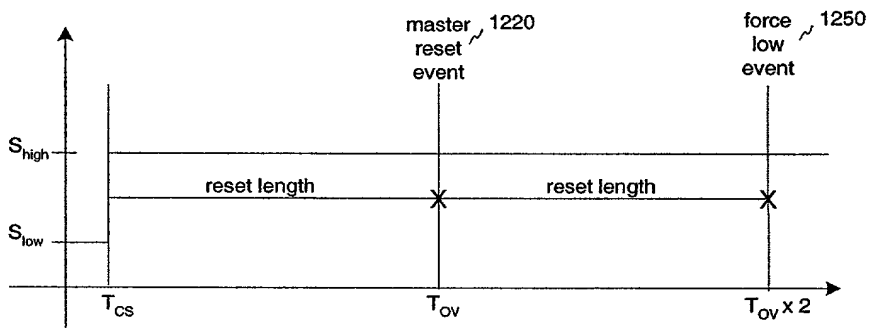


FIG. 12D

Binary Traversal Flowchart from a Tag Perspective

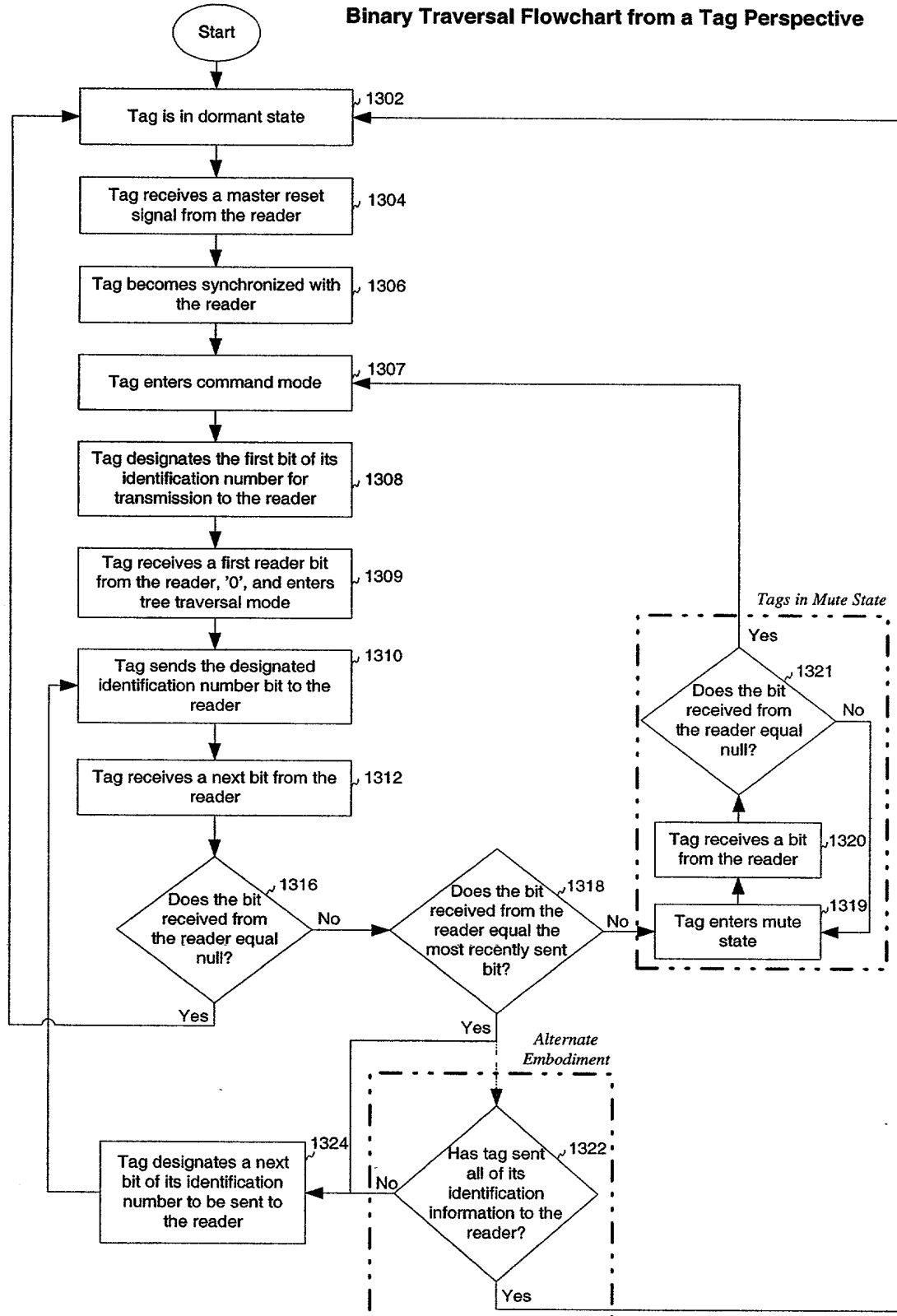


FIG.13

2001-20b.vsd/11

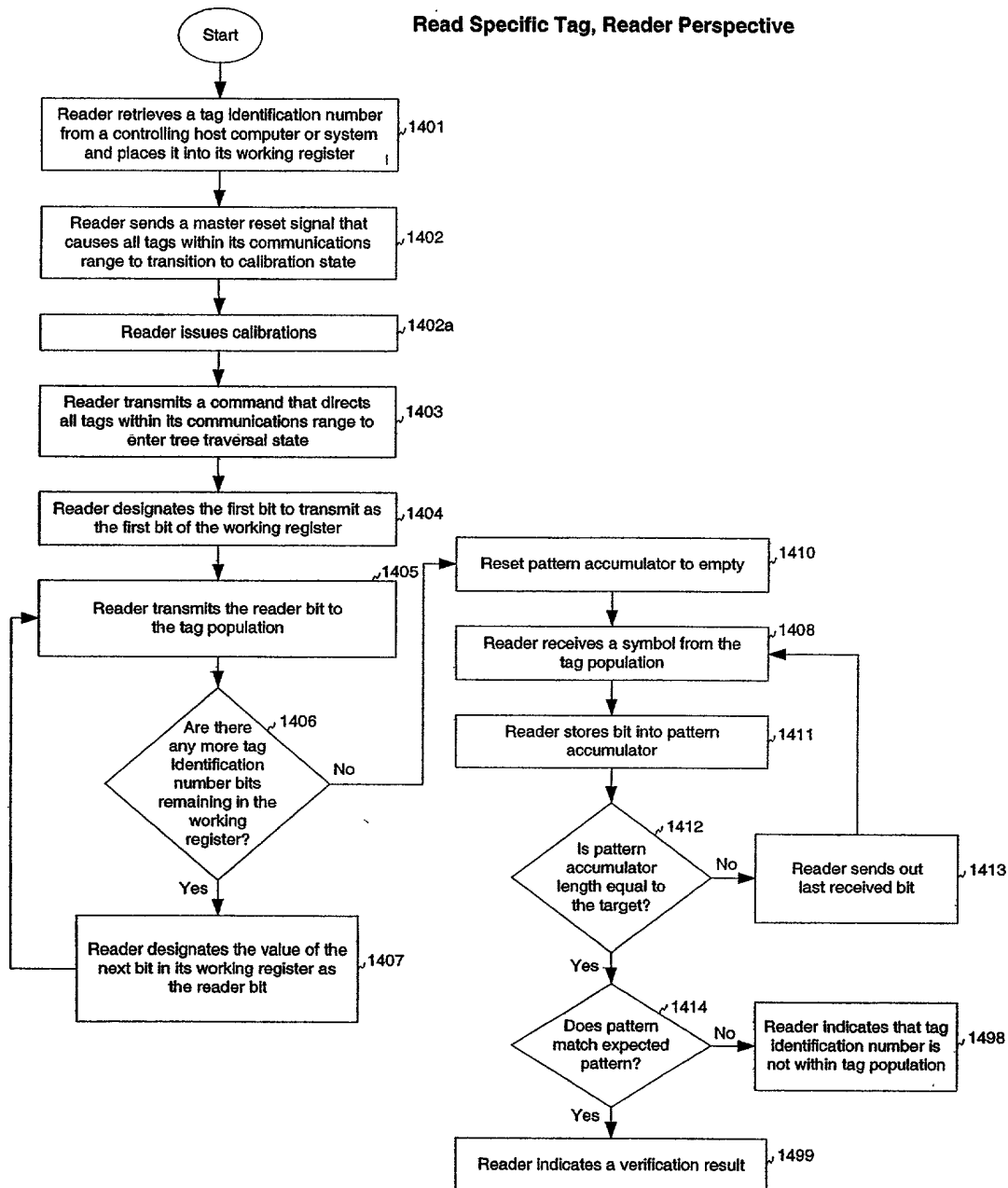


FIG. 14A

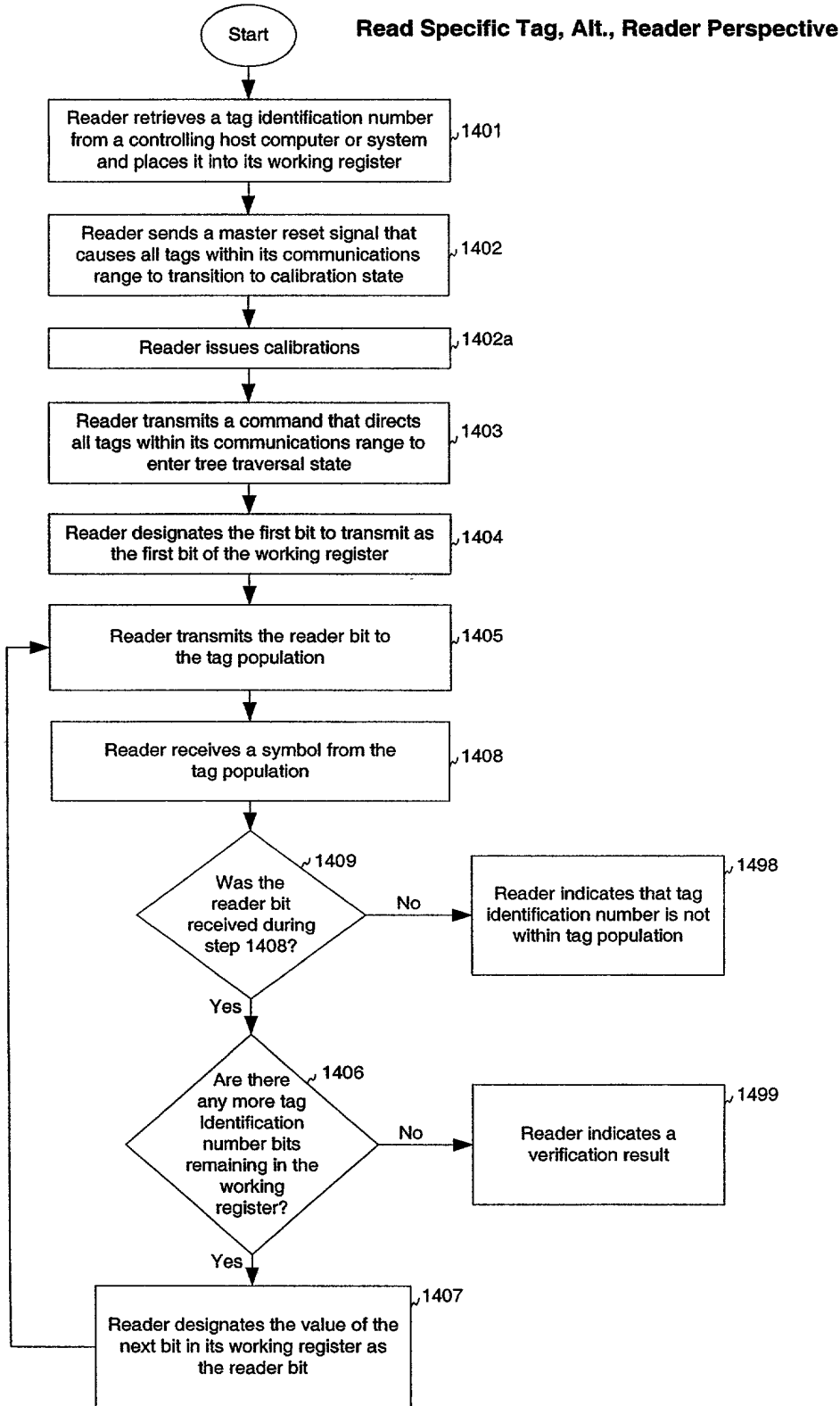


FIG. 14B

10072904-02200

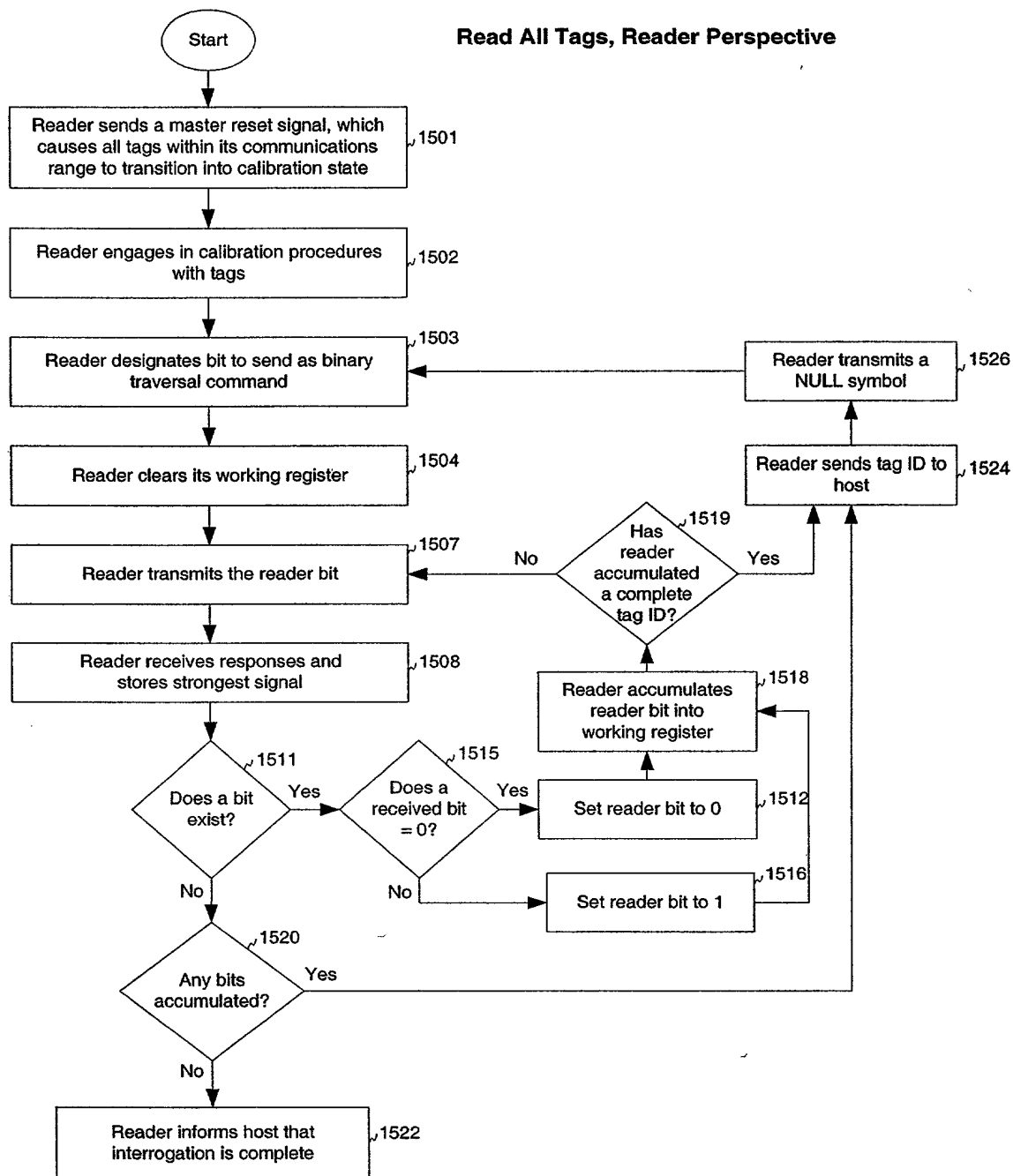


FIG. 15A :

Binary Traversal Paths and Tree for a 3-bit Tag Population

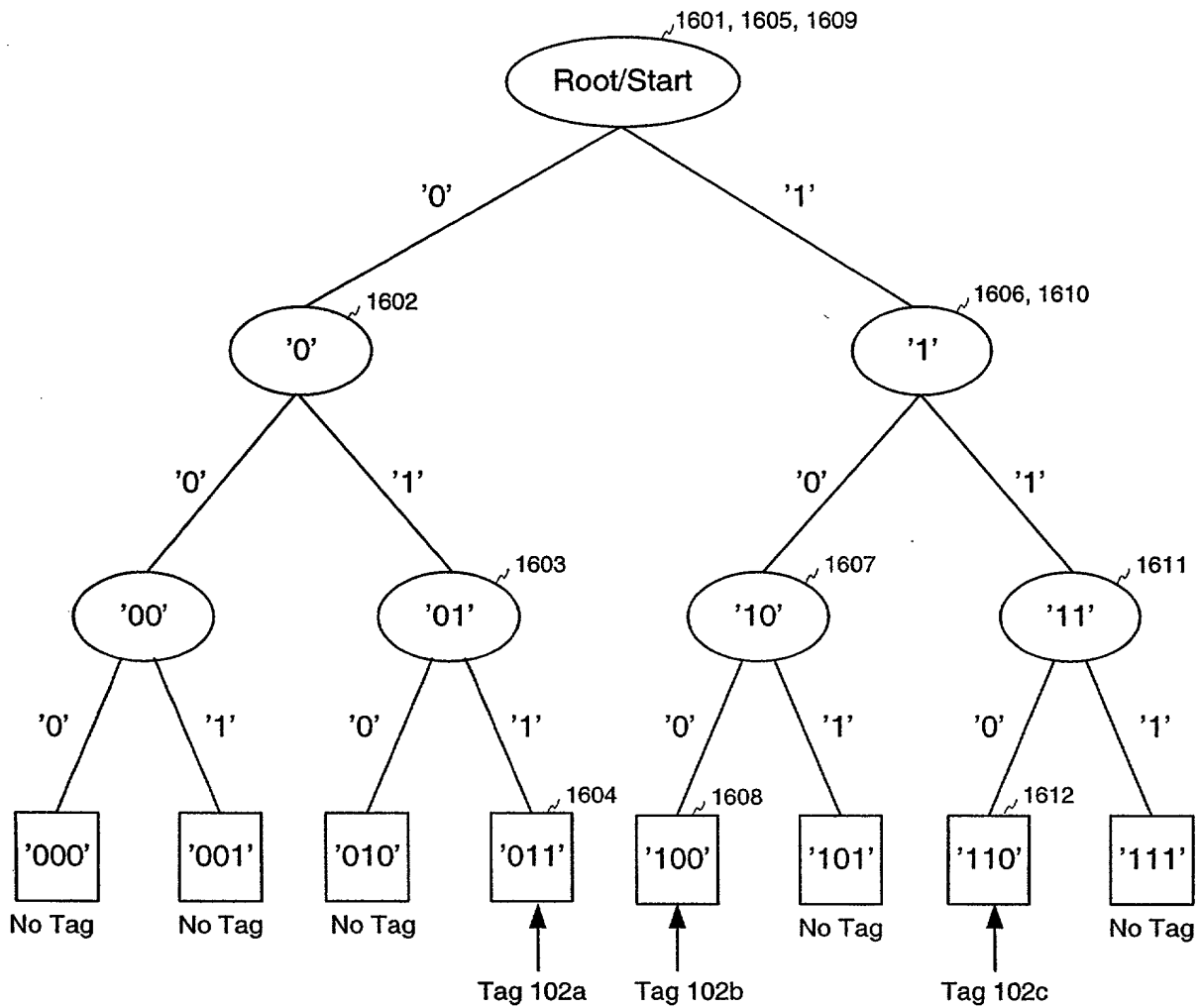


FIG. 16

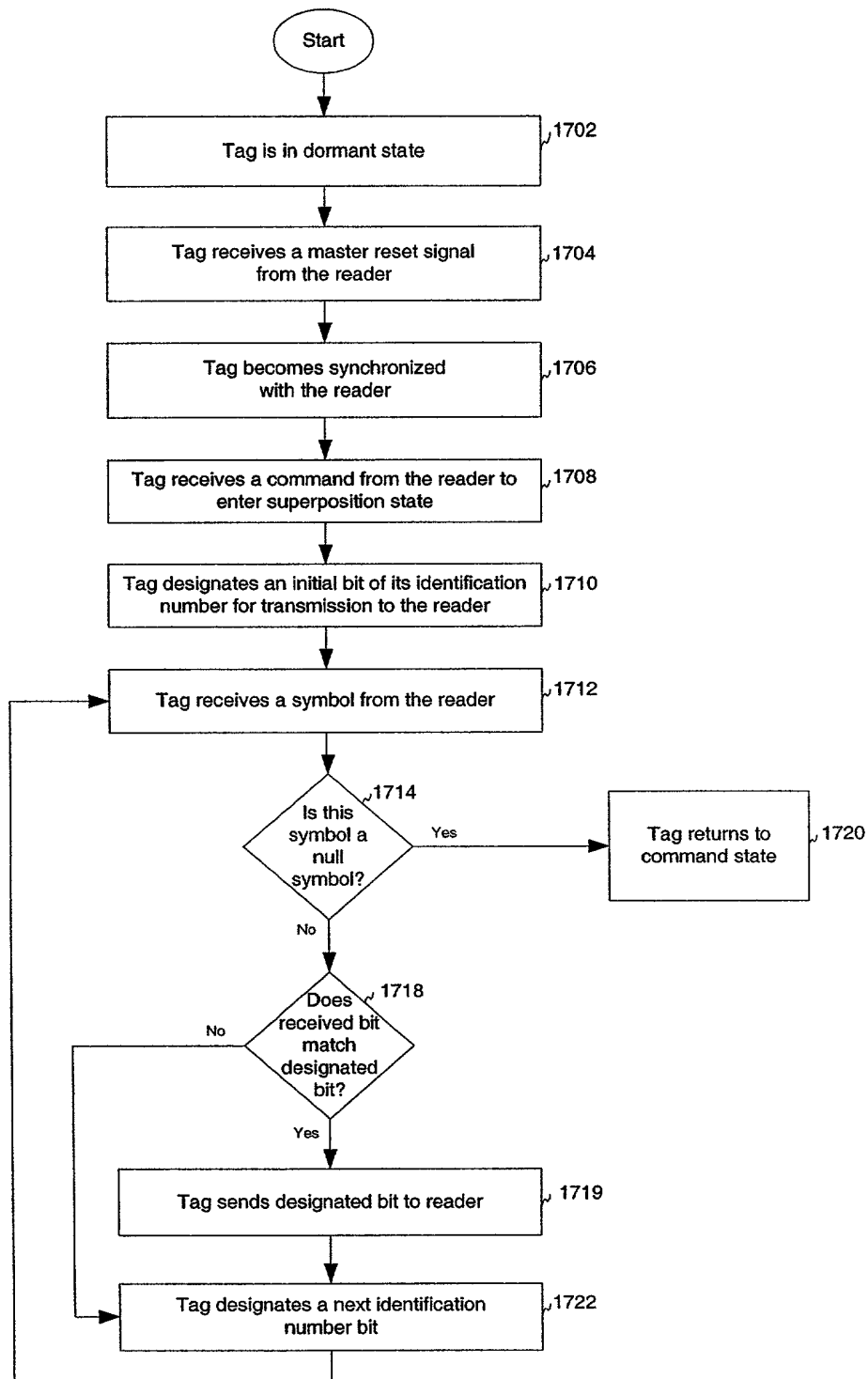


FIG. 17A

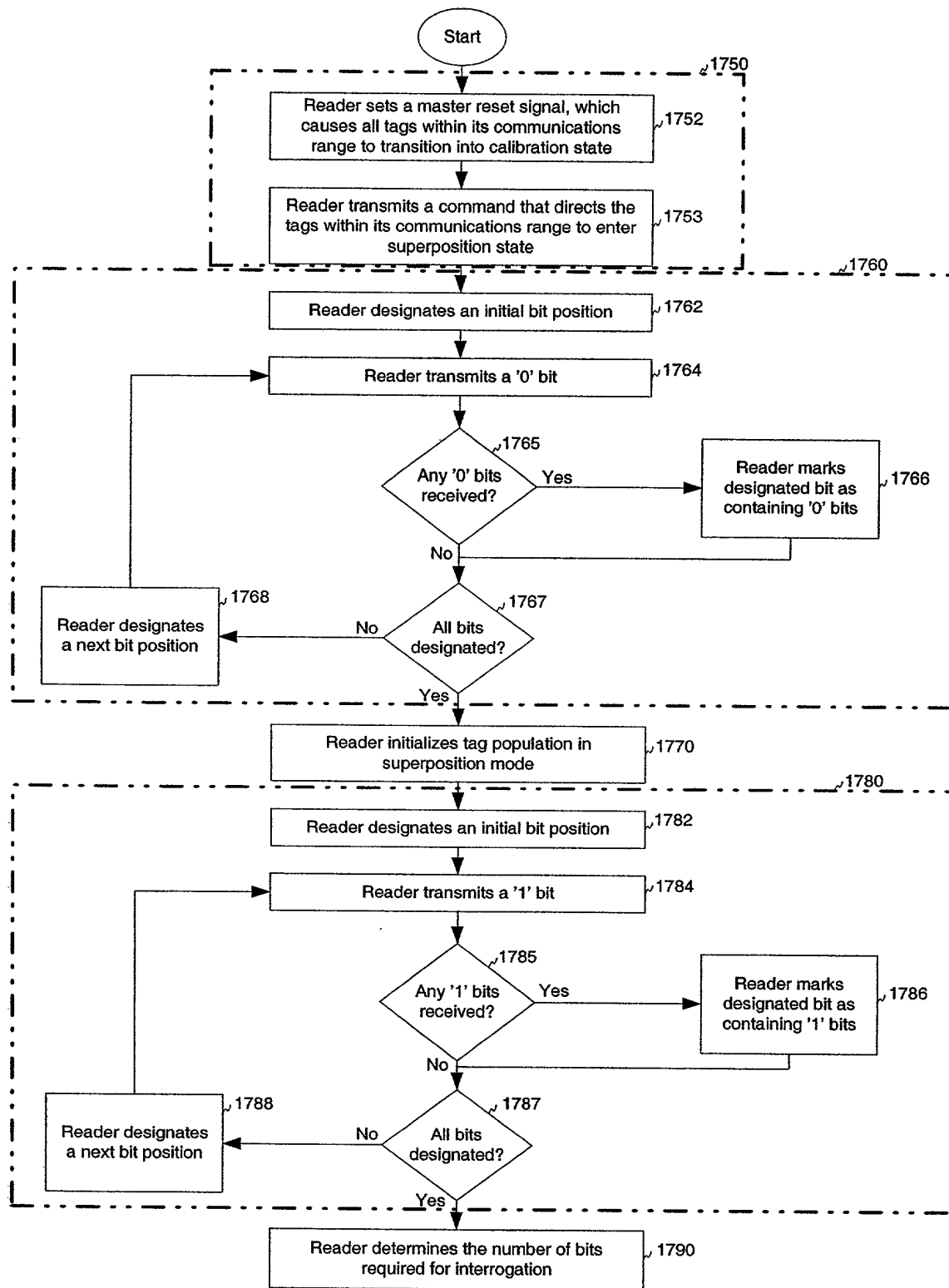


FIG. 17B

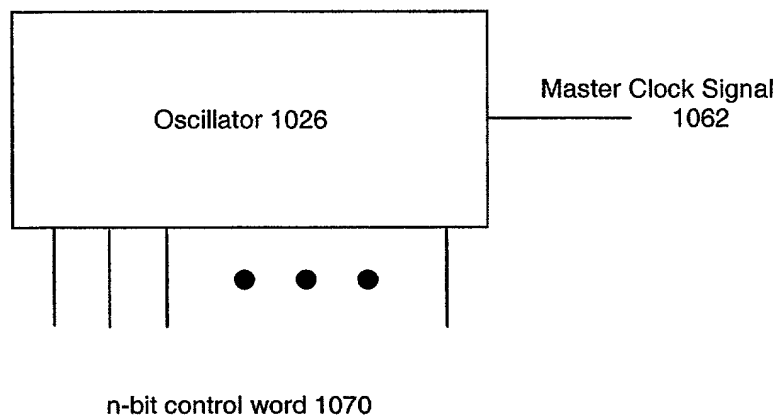


FIG. 18

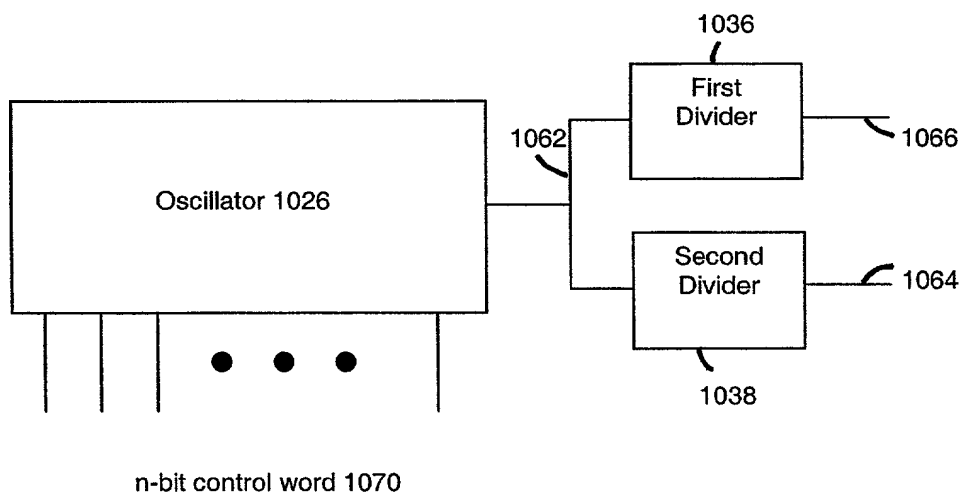


FIG. 19

2021-09-27 10:07:00

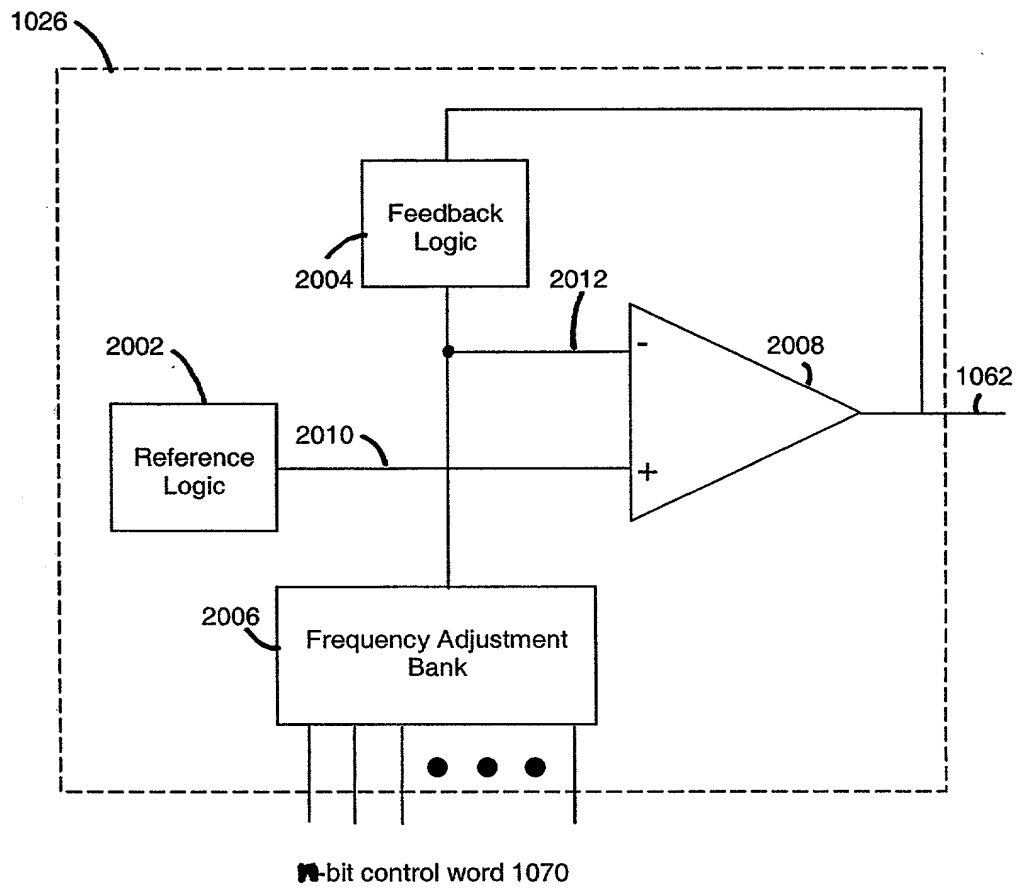


FIG. 20

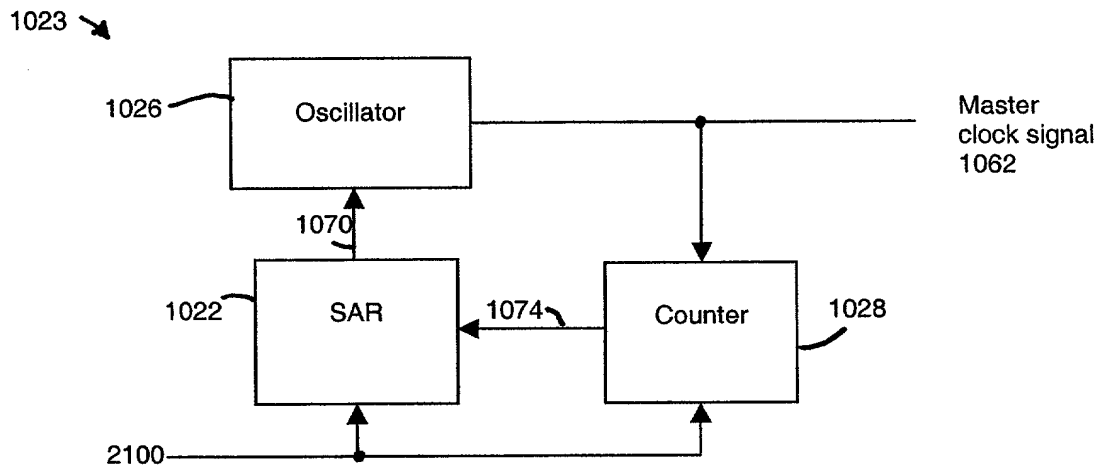


FIG. 21A

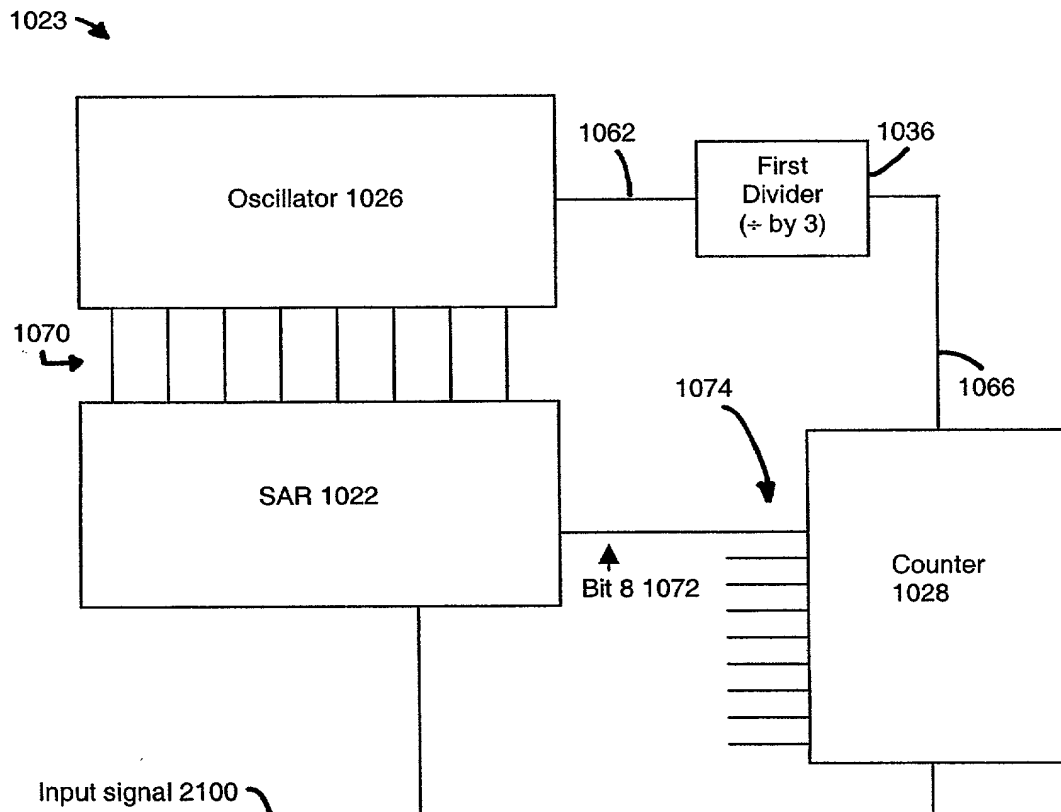


FIG. 21B

Conceptual implementation of SAR calibration

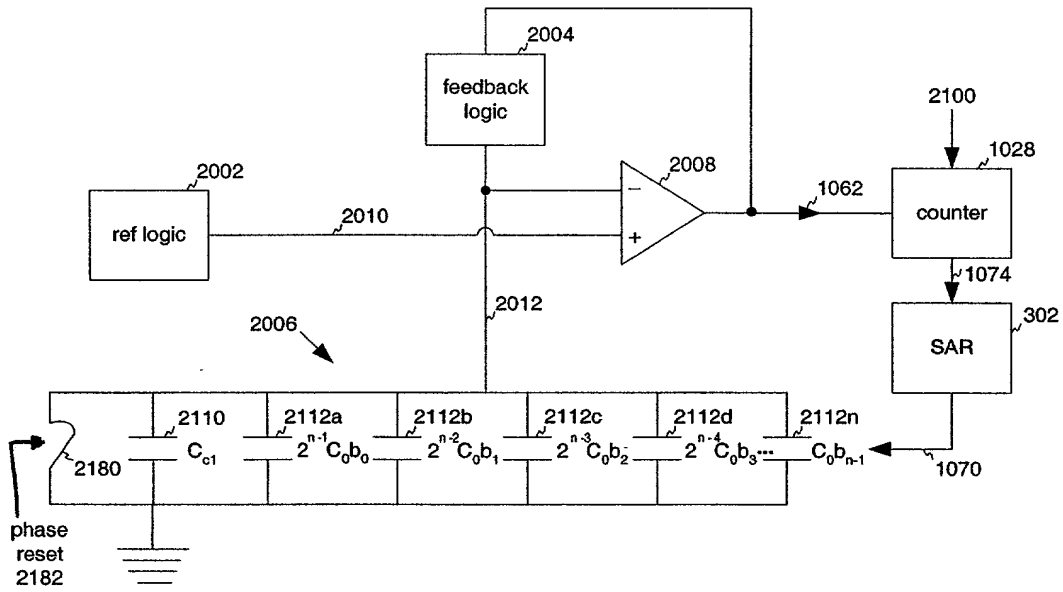


FIG. 21C

Physical implementation of adjustable capacitor bank

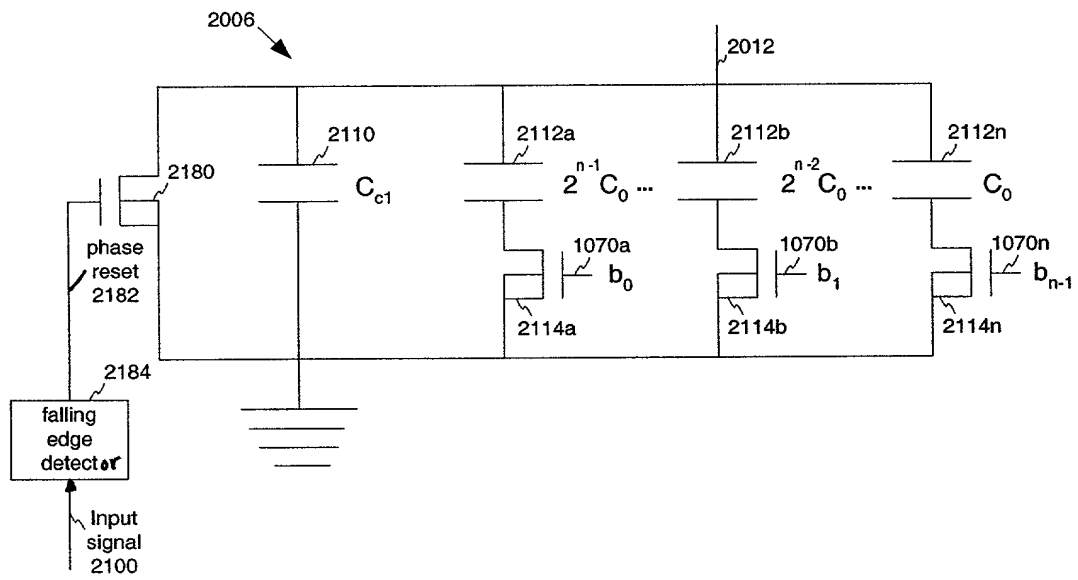
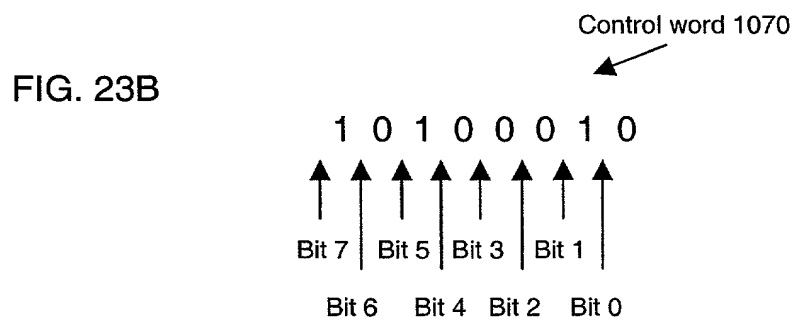
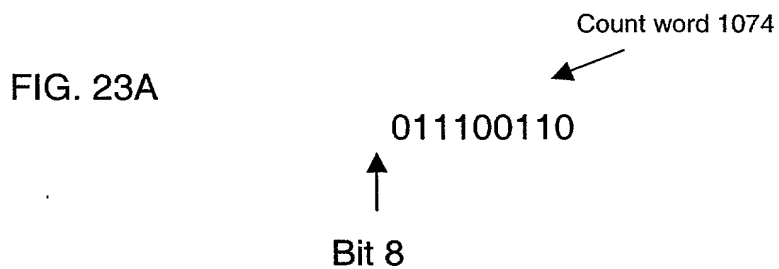
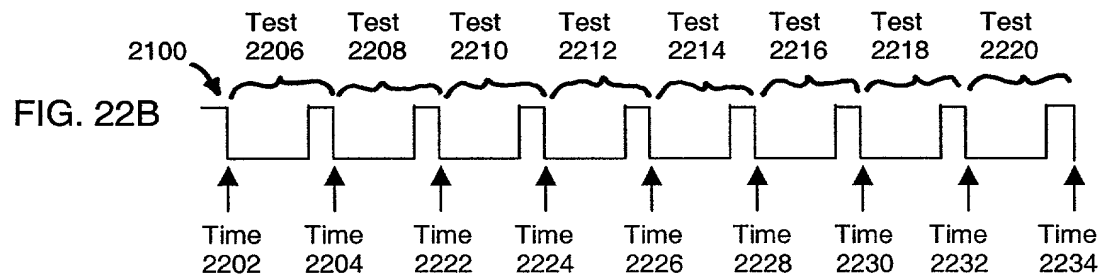
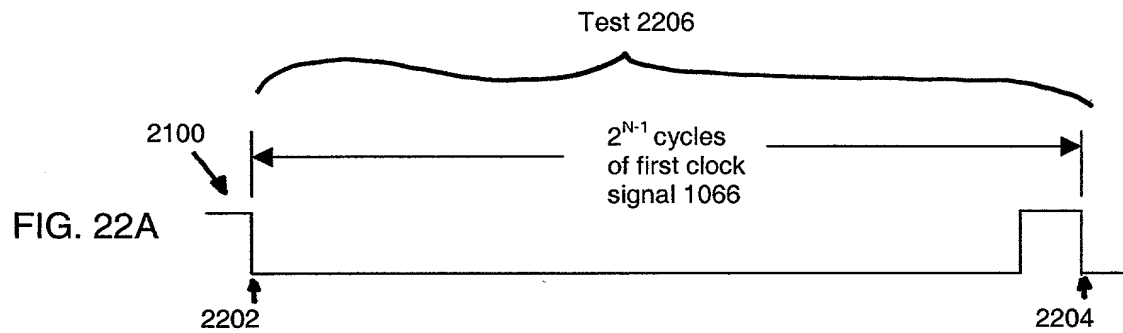


FIG. 21D



2021-01-19 15:22:00

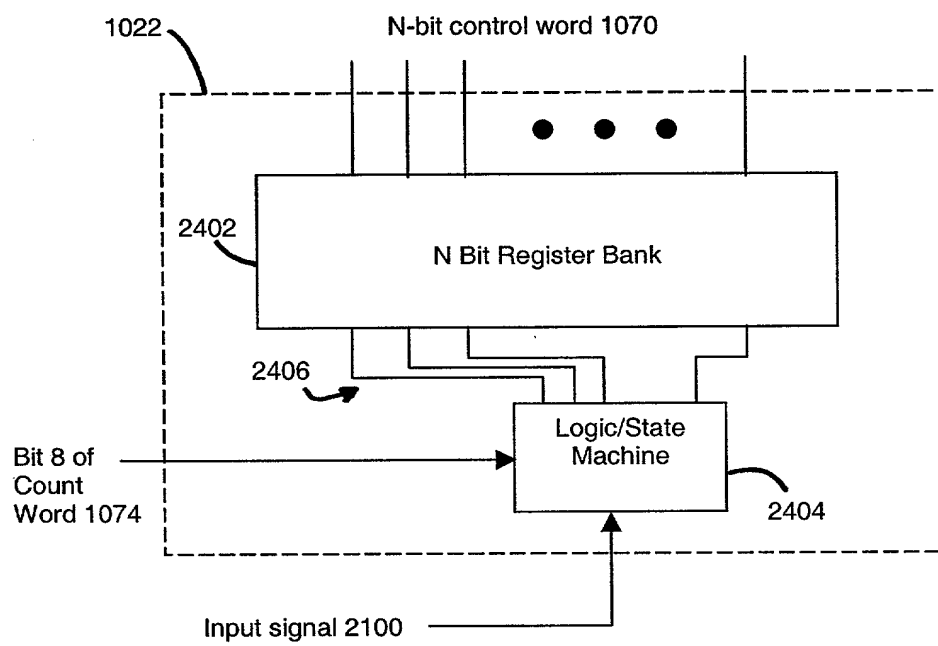


FIG. 24

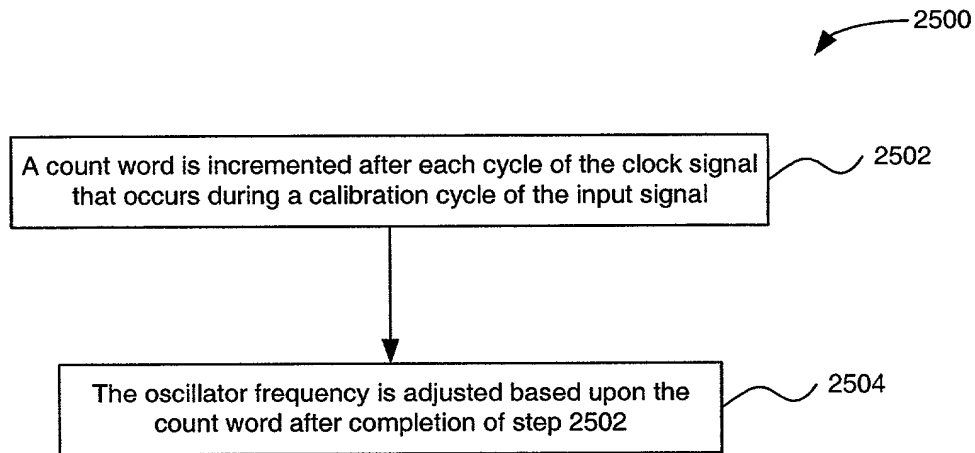


FIG. 25A

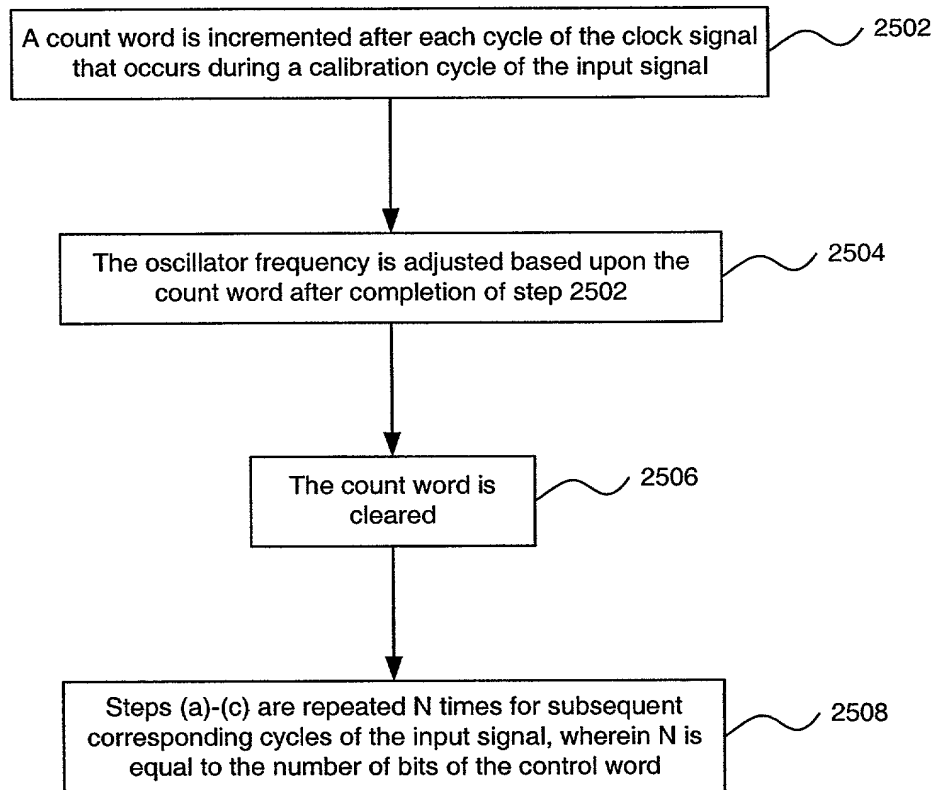


FIG. 25B

20072904-02-002

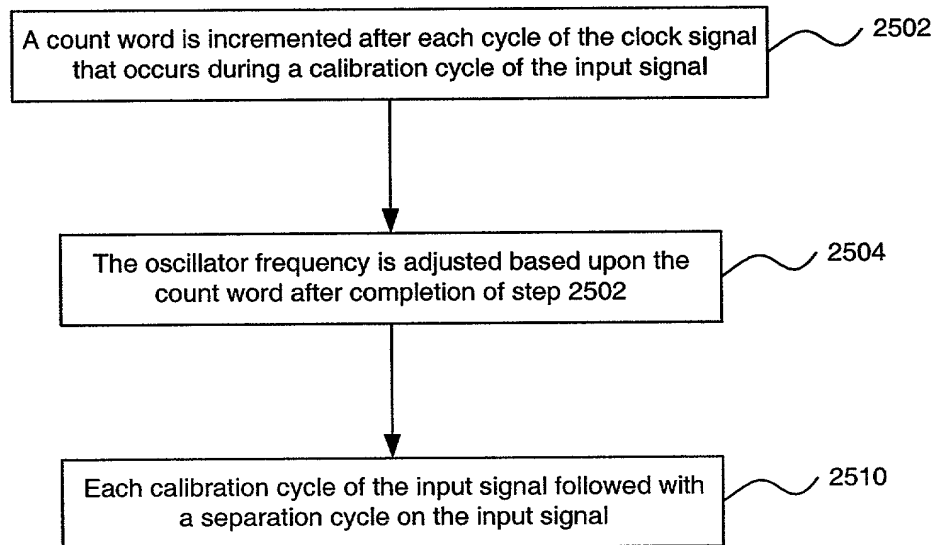


FIG. 25C

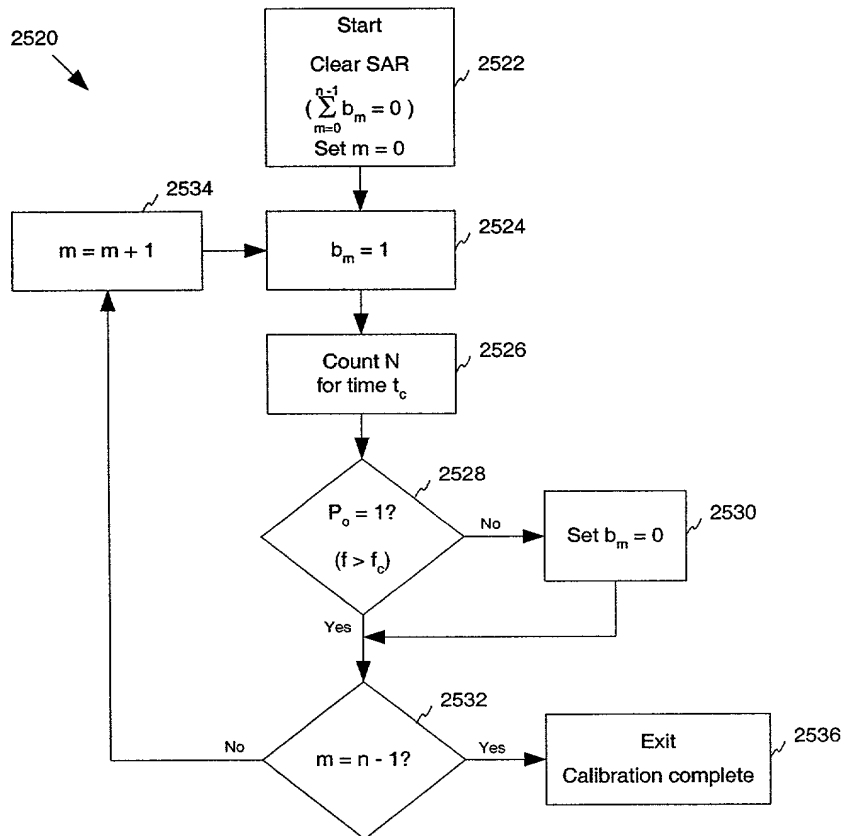


FIG. 25D

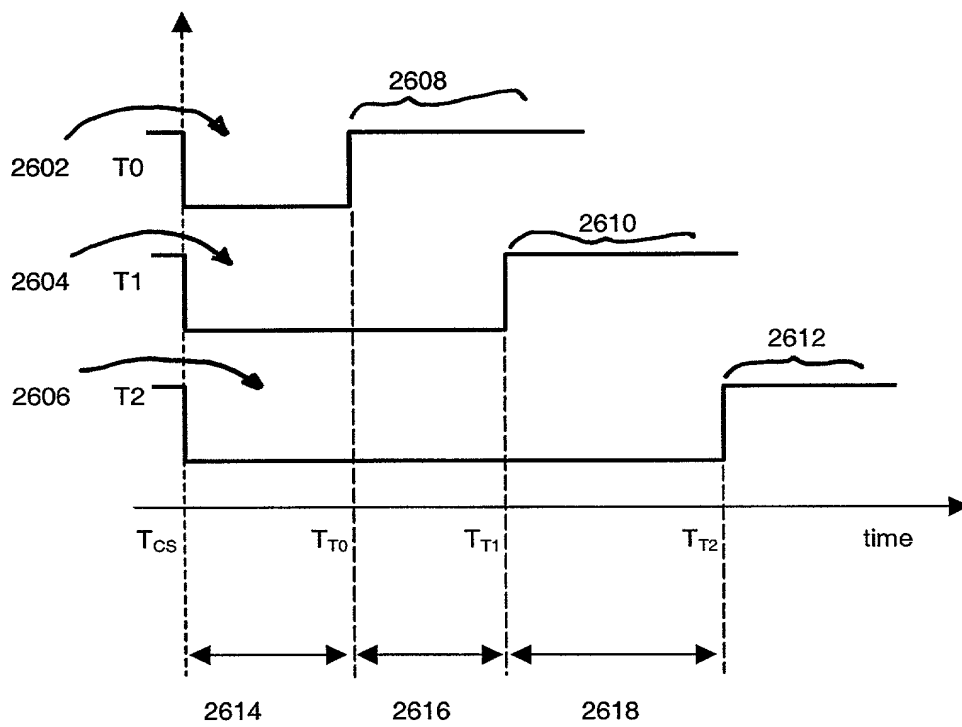


FIG. 26A

Data Symbol Timing Chart

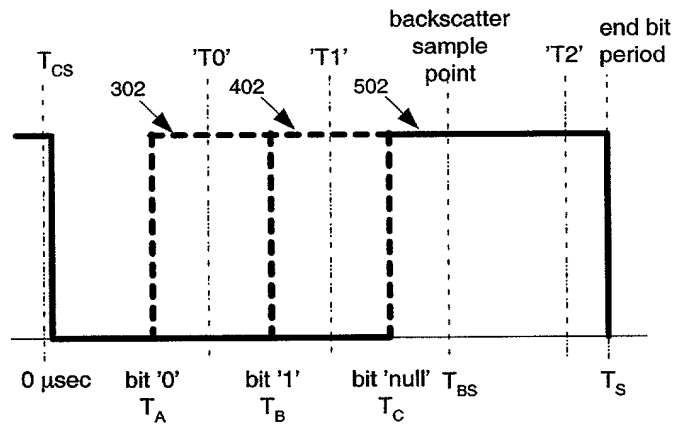


FIG. 26B

20250404-0100

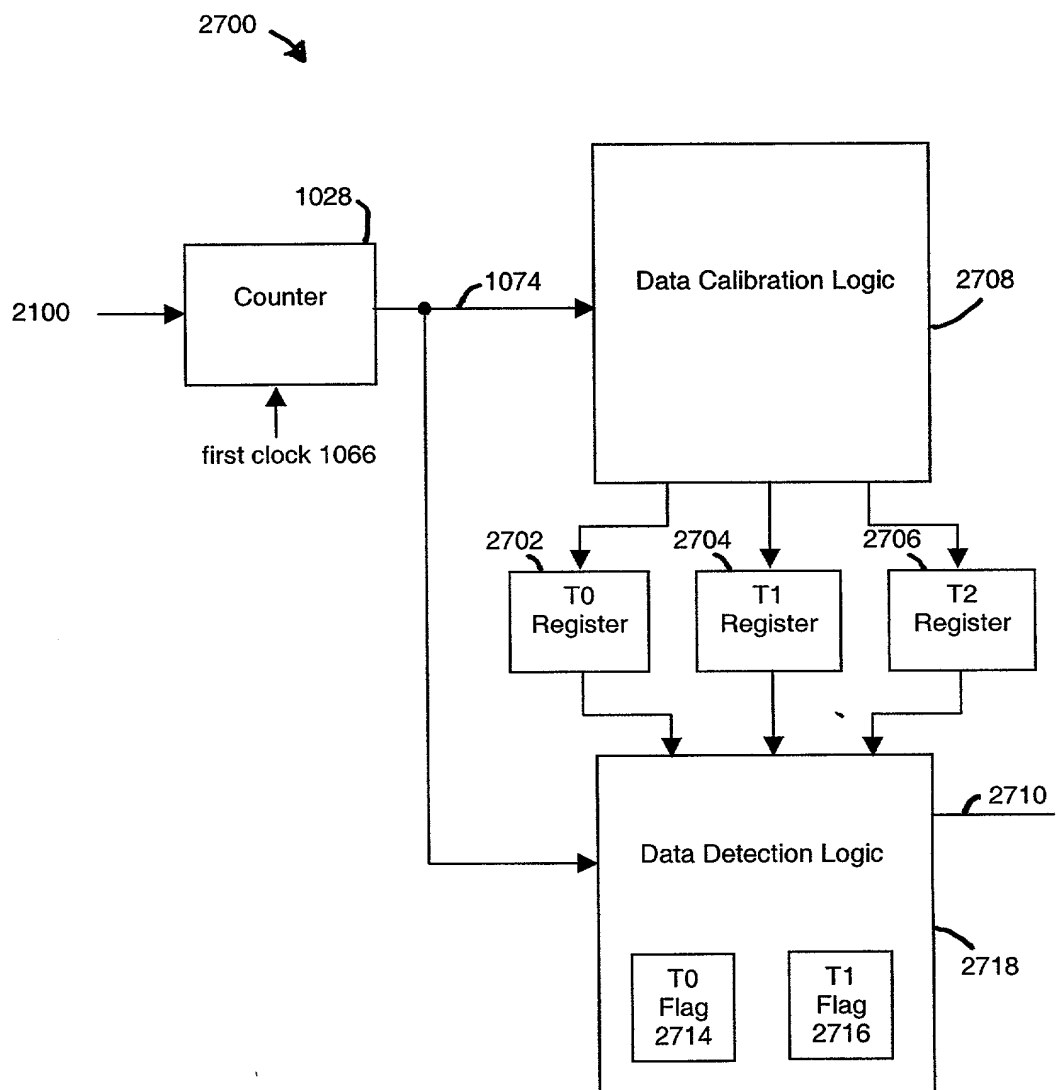


FIG. 27

20250413022007

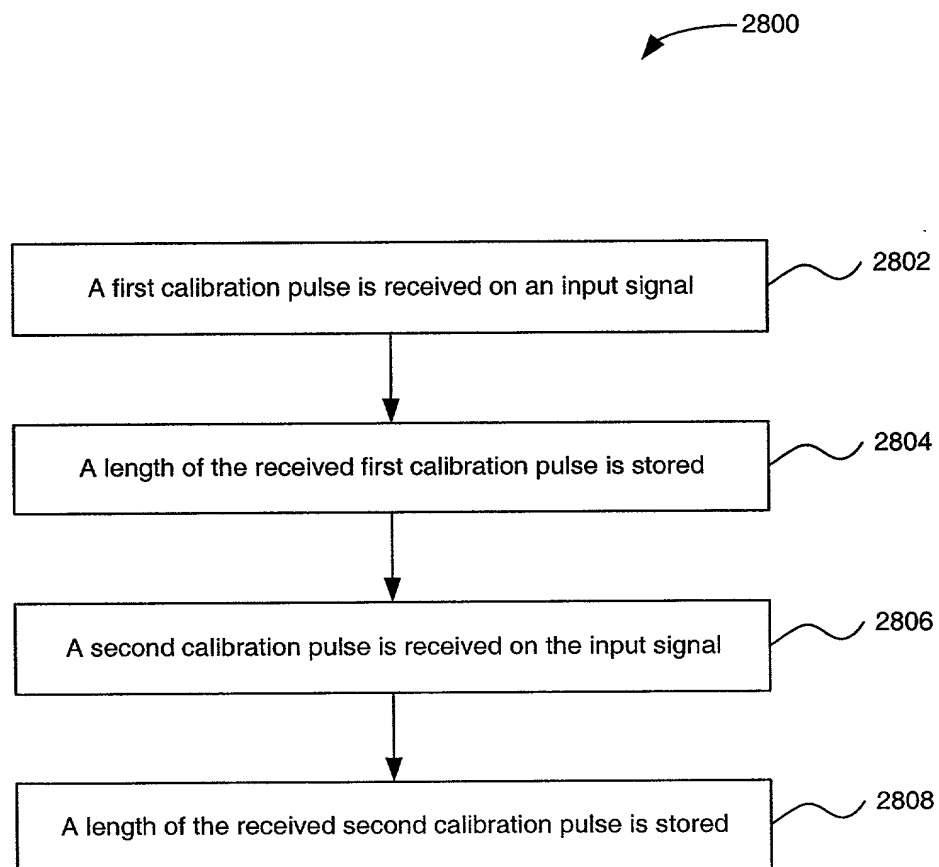


FIG. 28A

From flowchart 2800

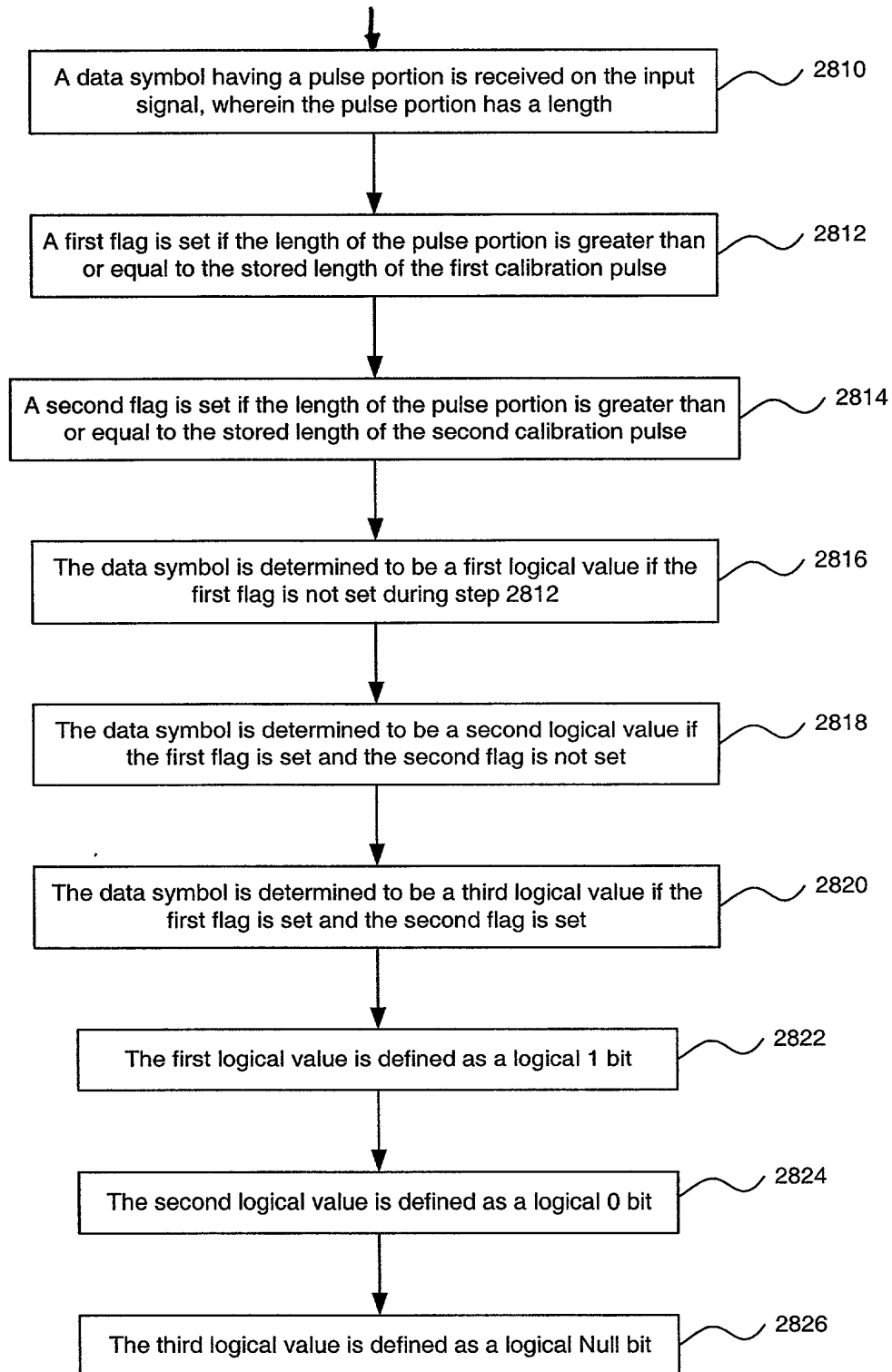


FIG. 28B

From flowchart 2800

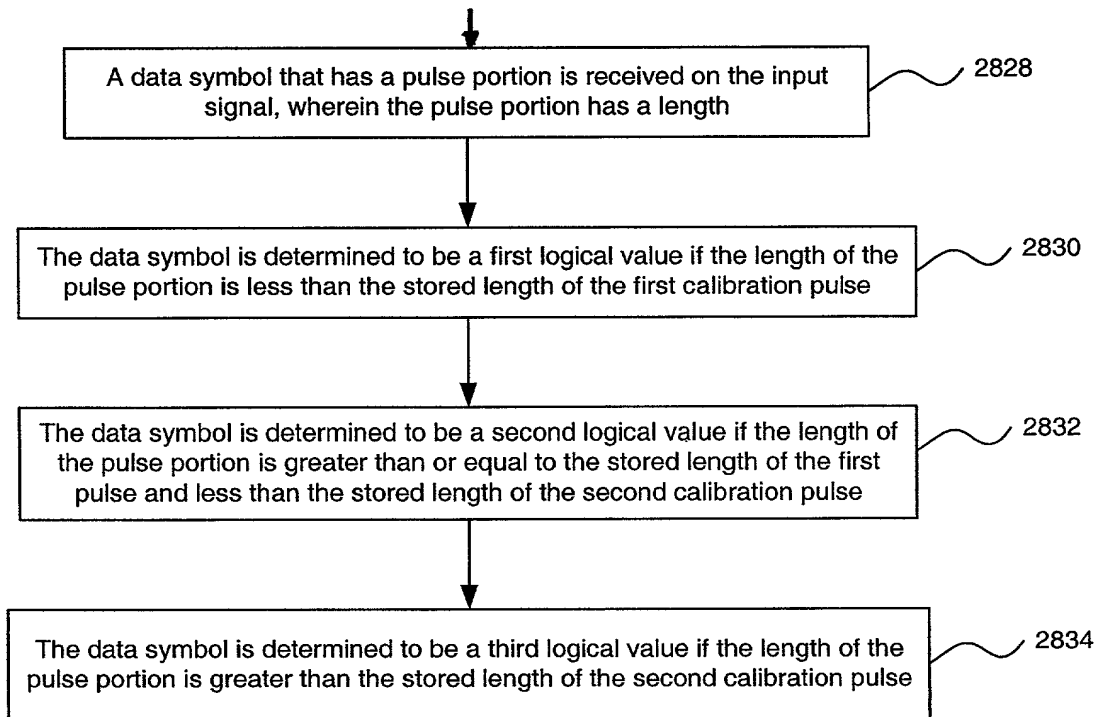


FIG. 28C

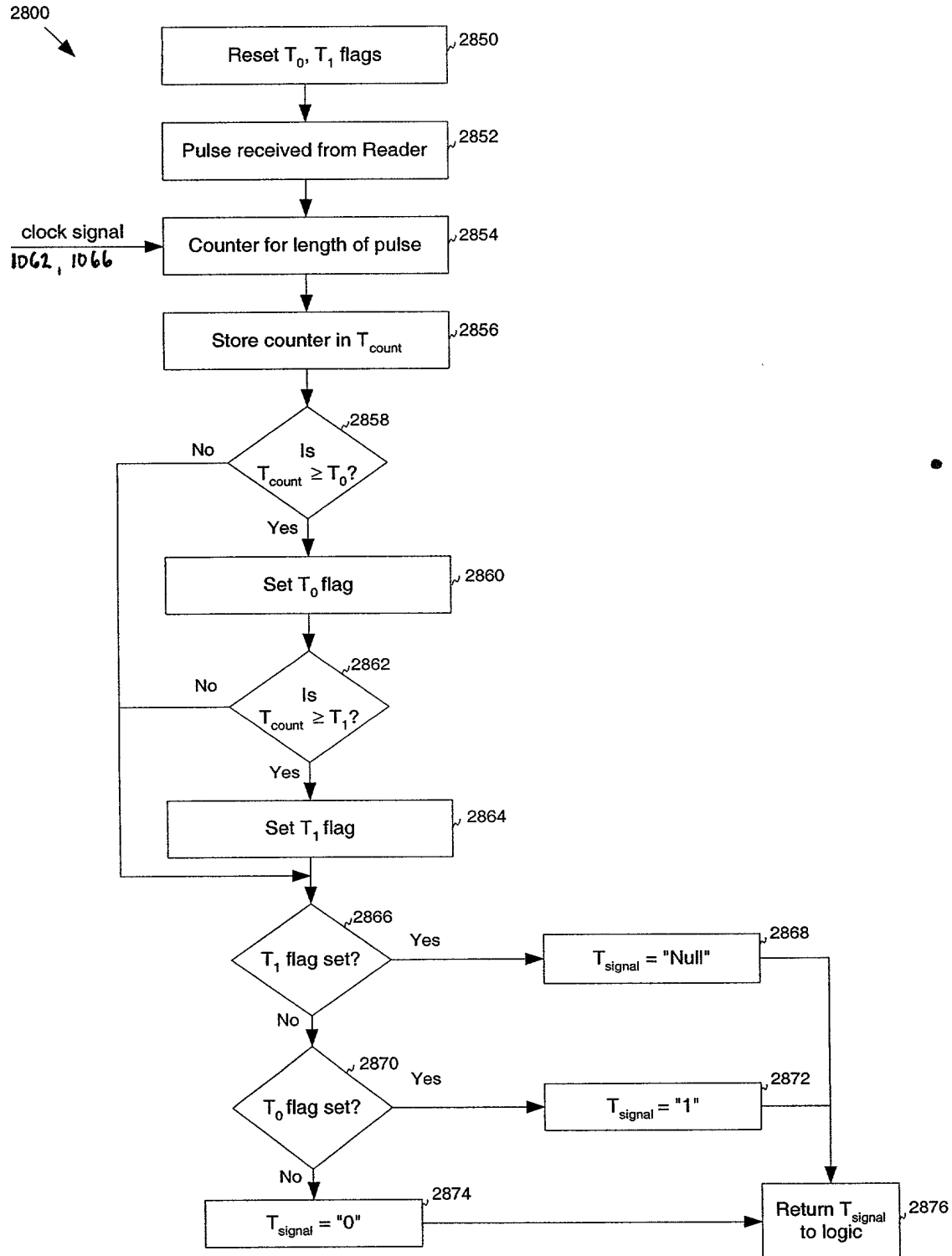


FIG. 28E

2800

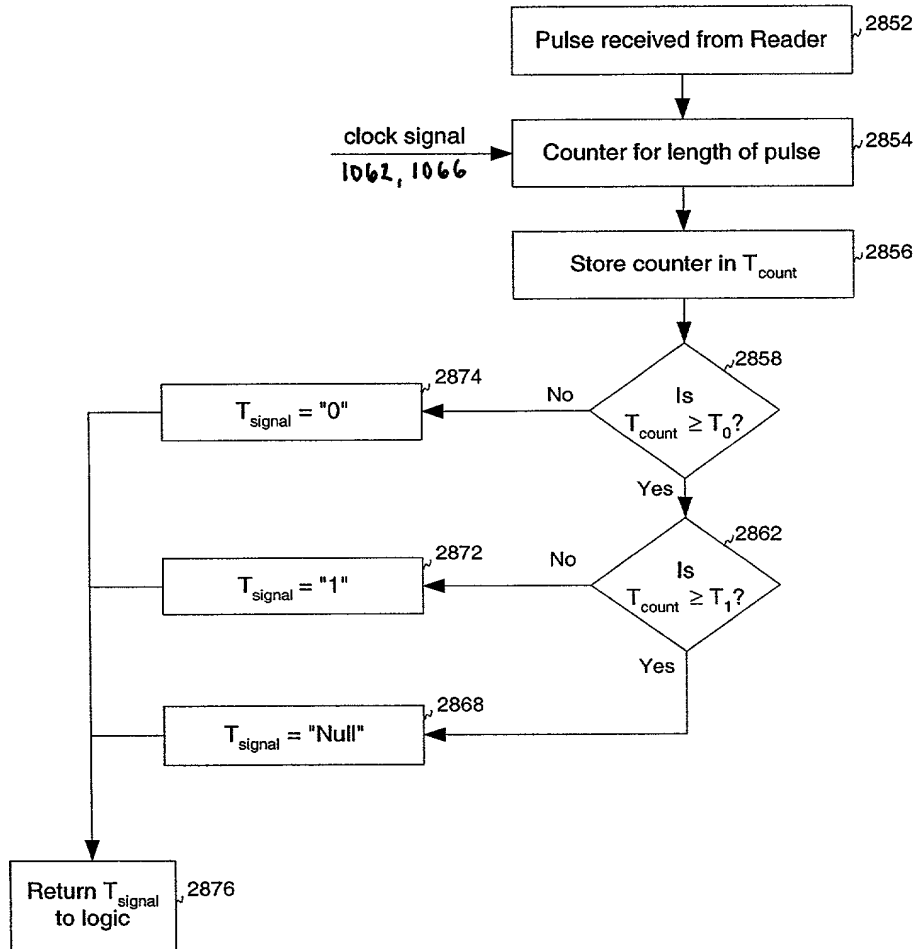
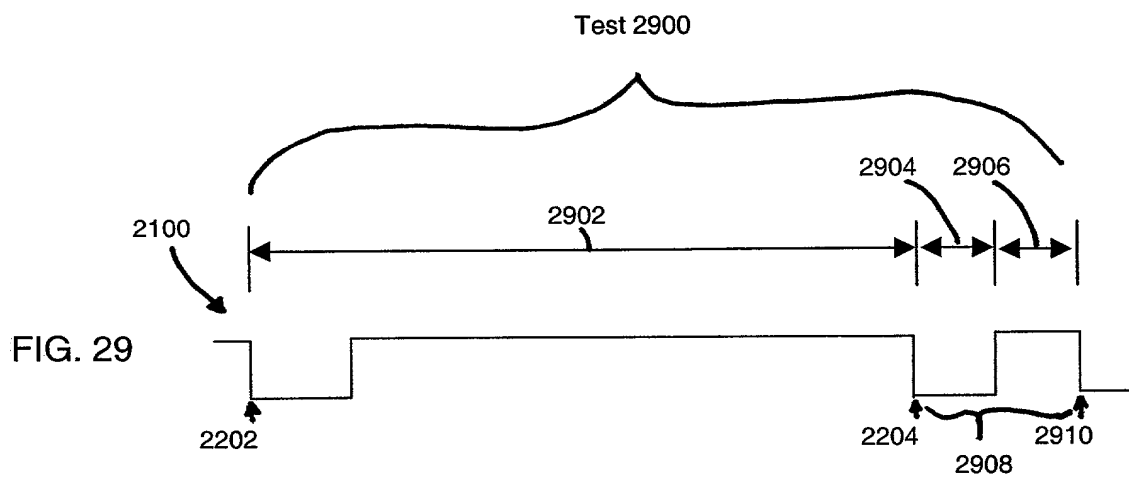


FIG. 28F



Analog Front End

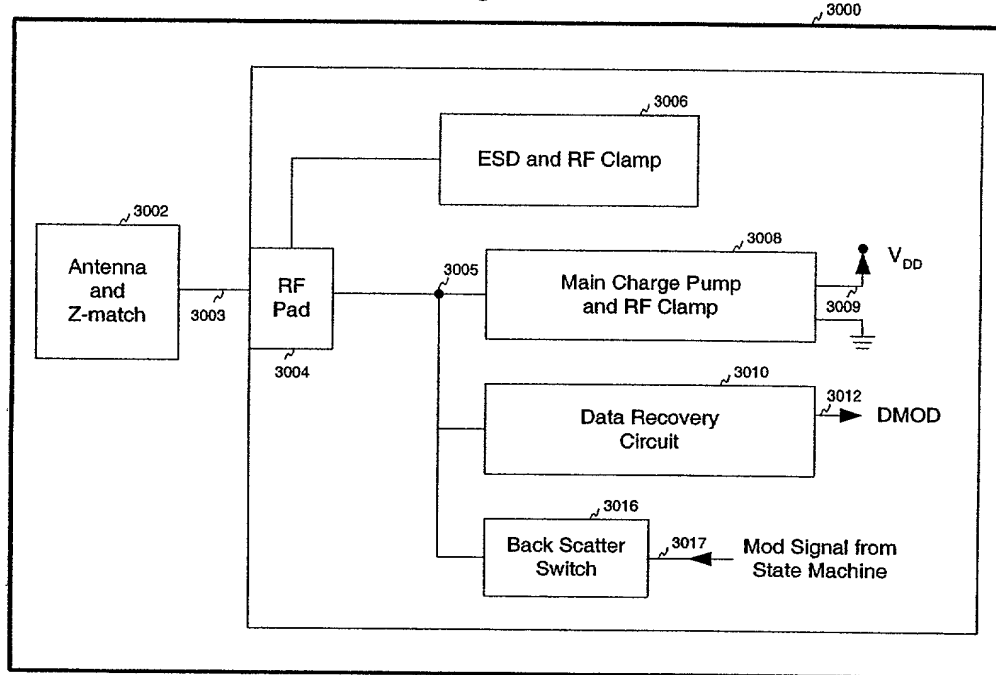


FIG. 30

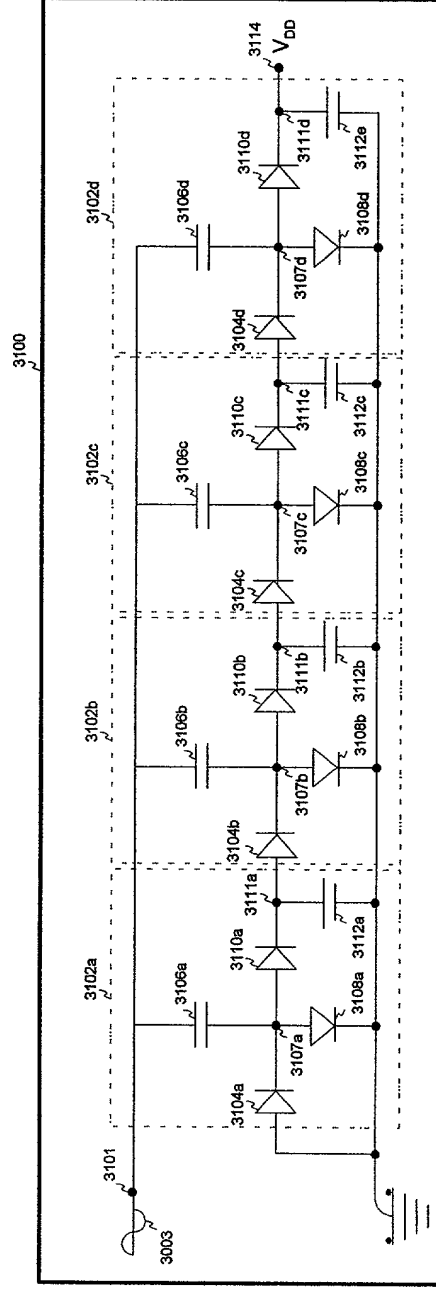


FIG. 31

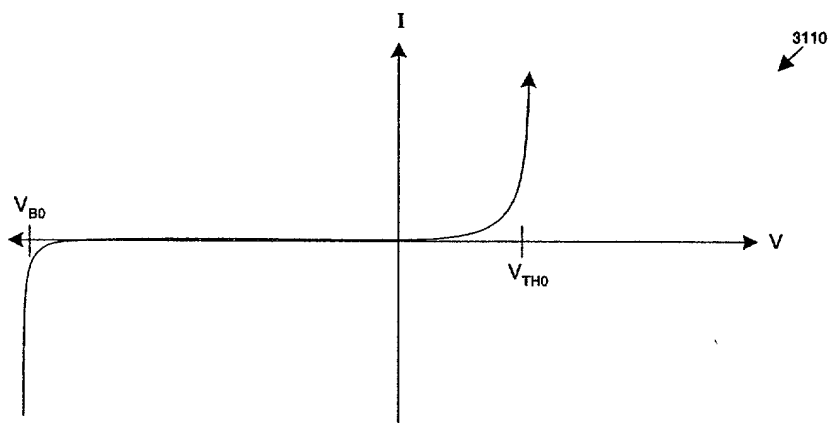


FIG. 32A

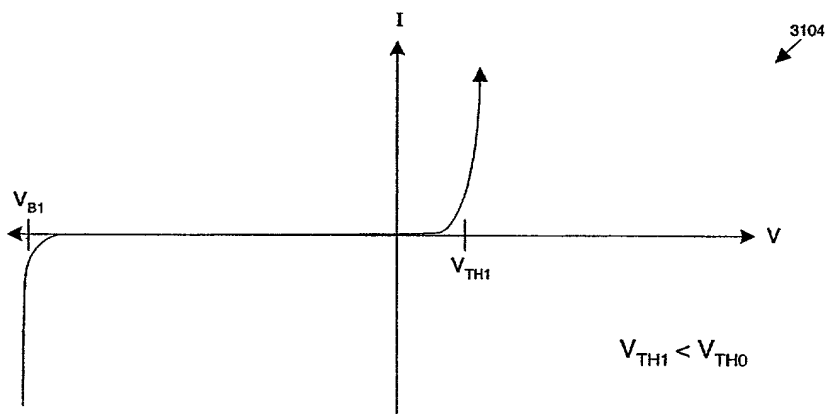


FIG. 32B

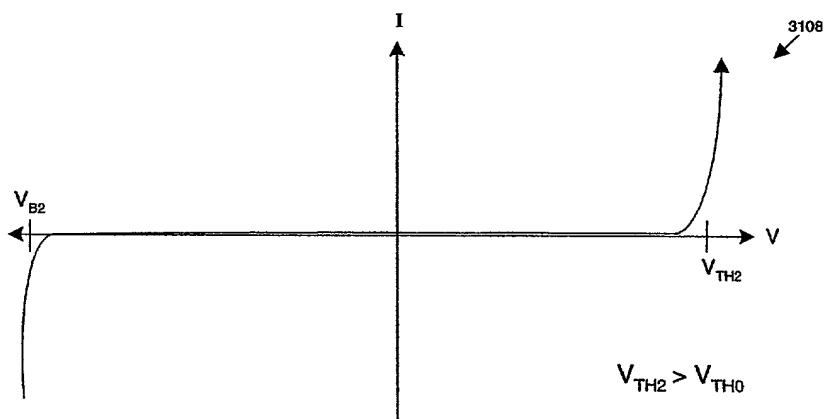


FIG. 32C

202504-1852/001

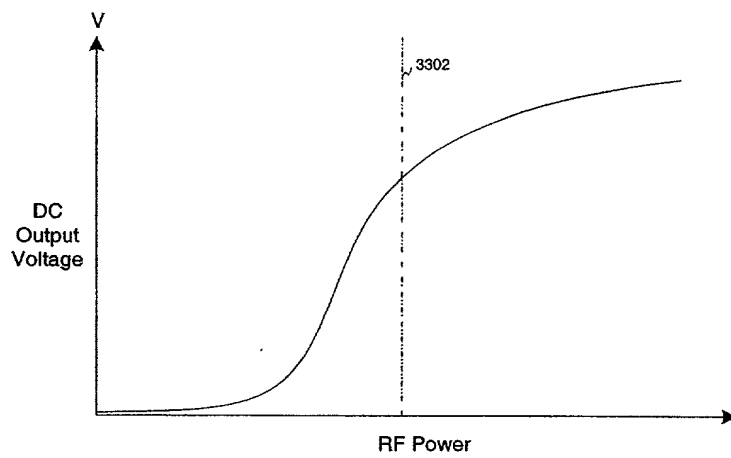


FIG. 33A

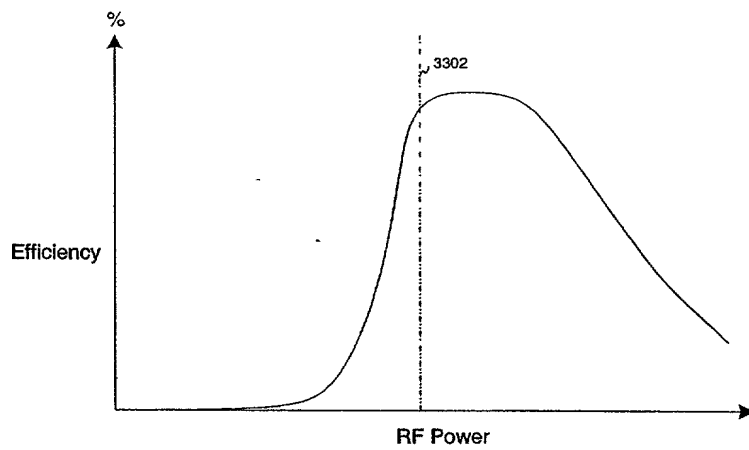


FIG 33B

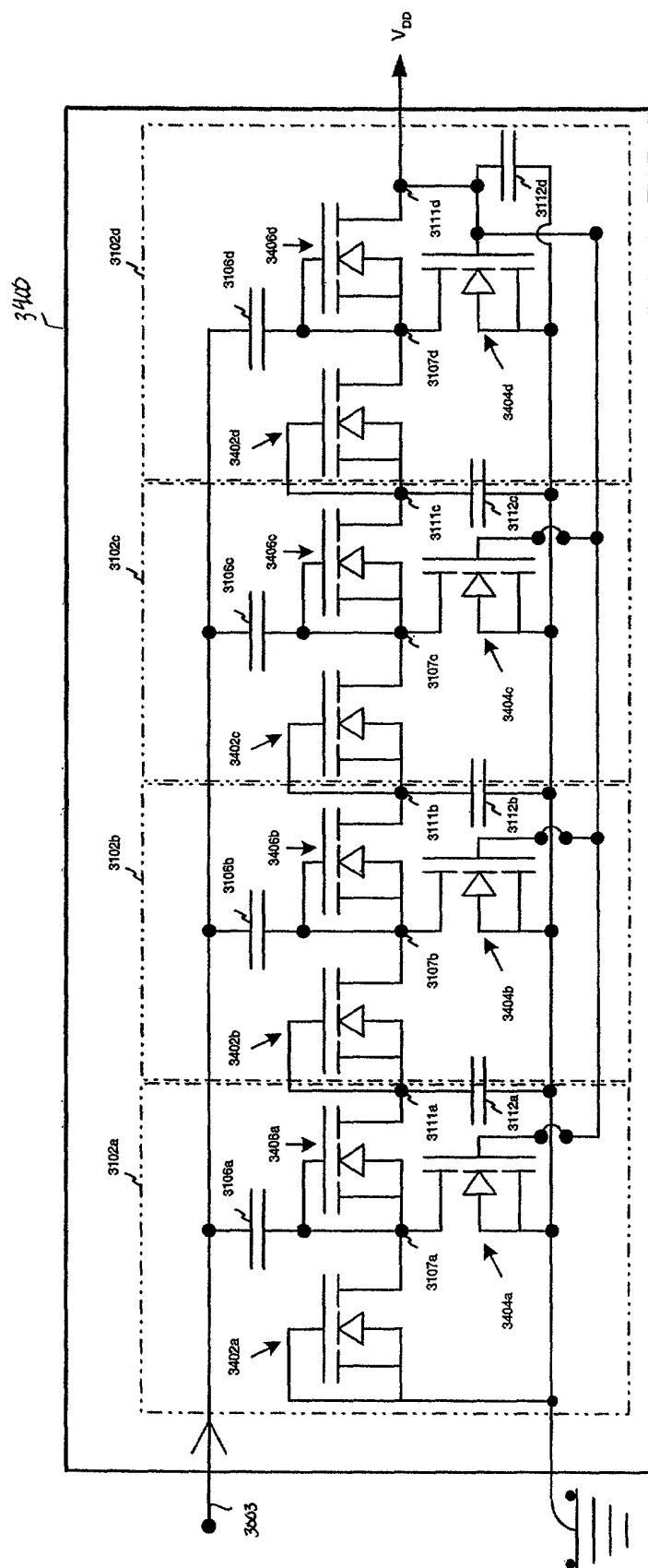


FIG. 34A

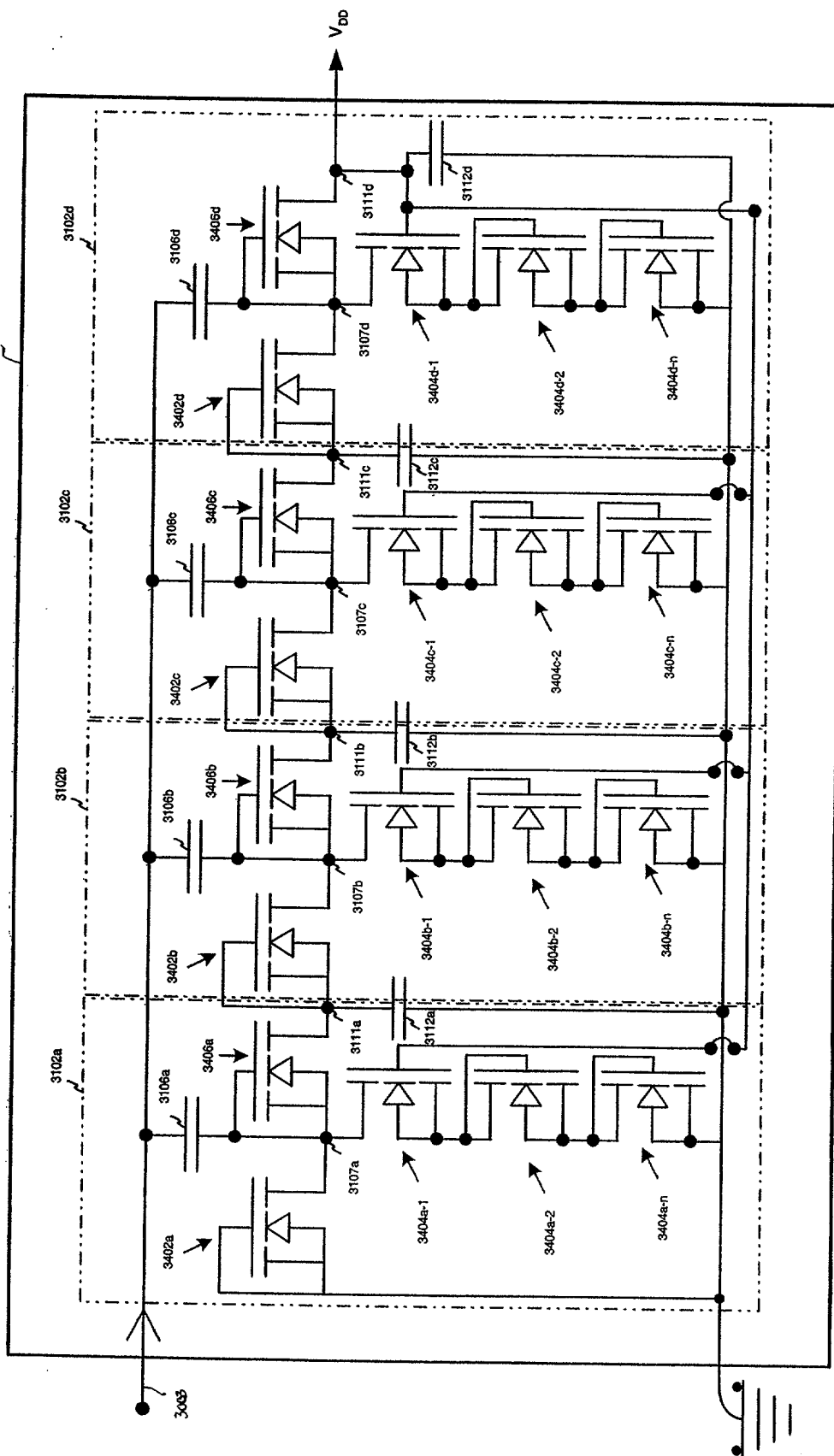


FIG. 34B

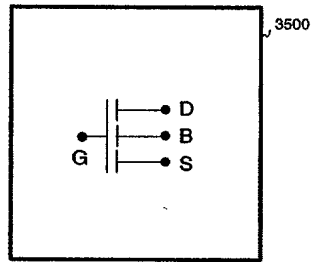


FIG. 35A

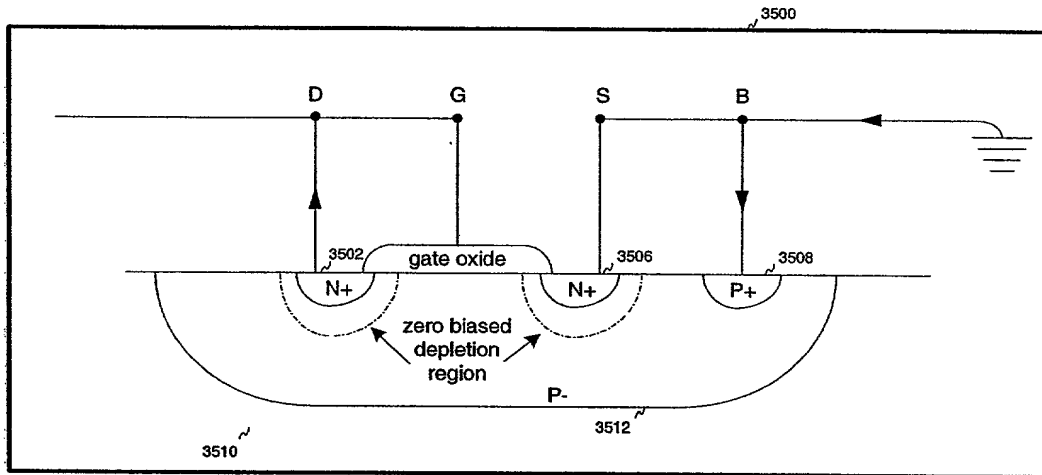


FIG. 35B

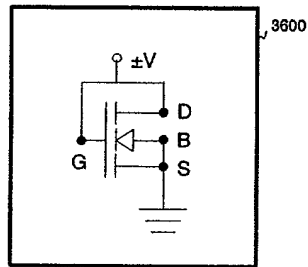


FIG. 36A

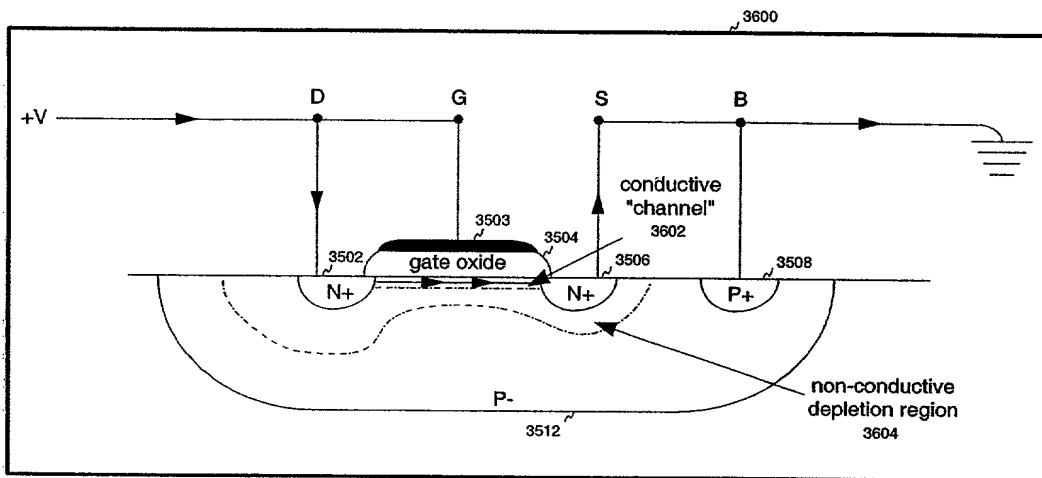


FIG. 36B

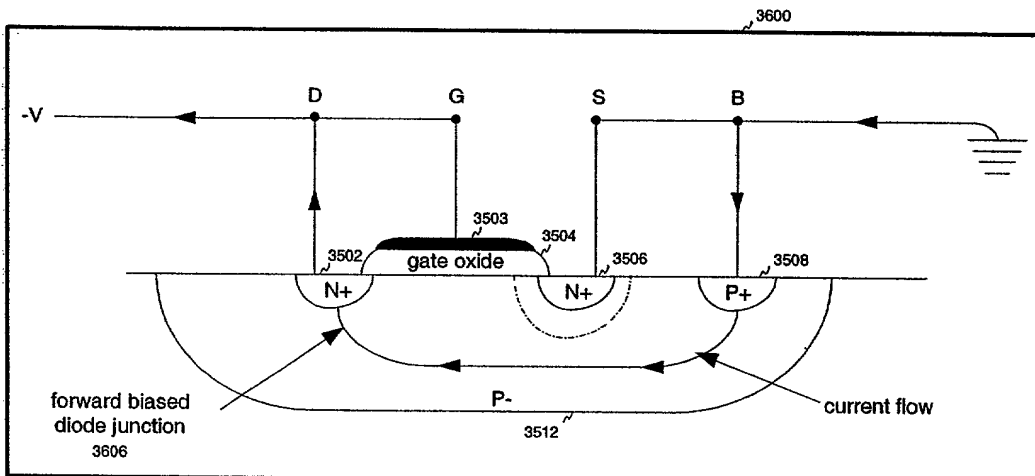


FIG. 36C

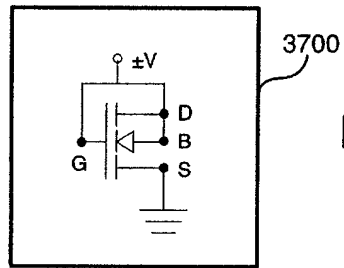


FIG. 37A

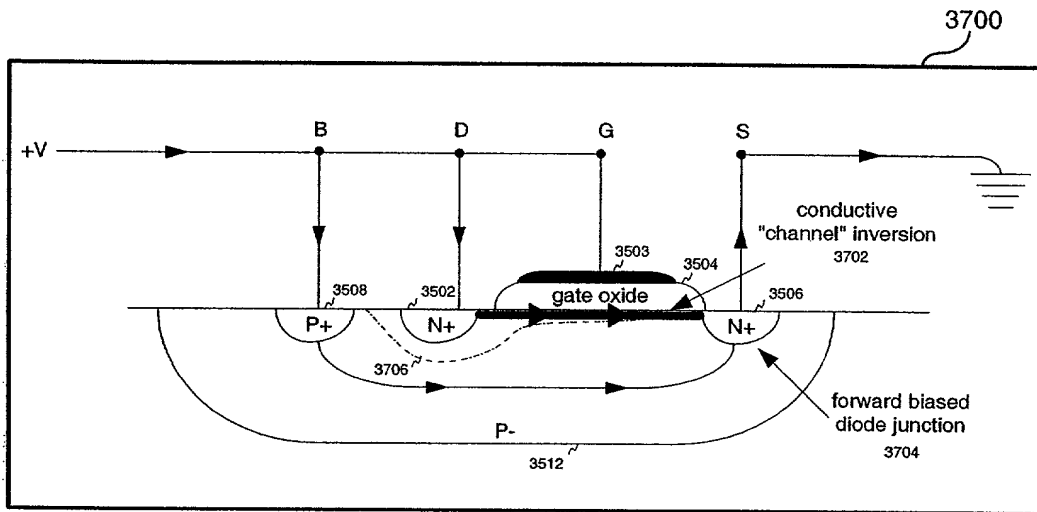


FIG. 37B

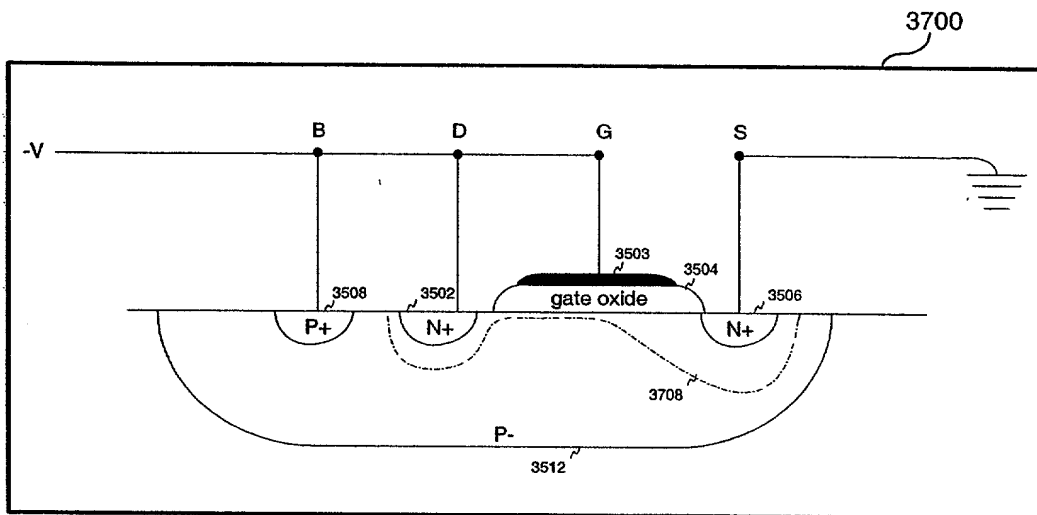


FIG. 37C

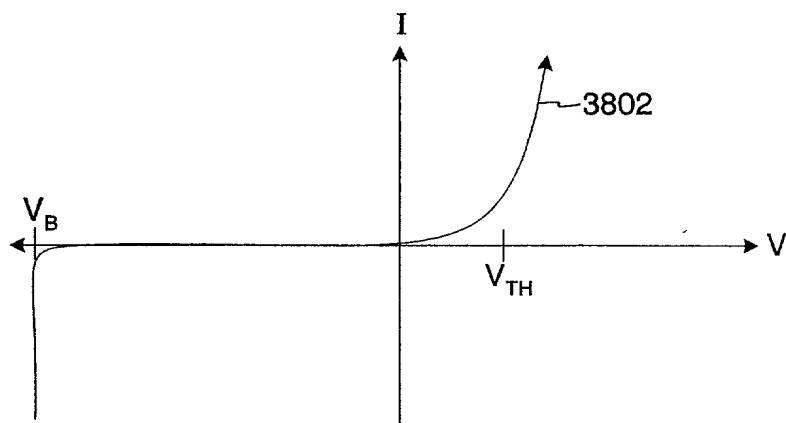


FIG. 38A

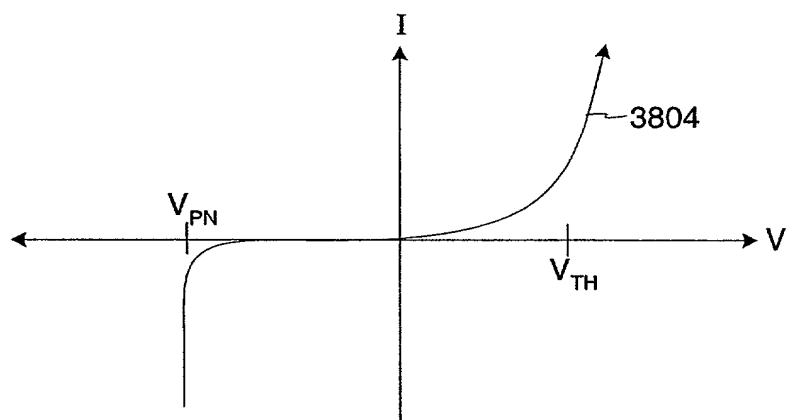


FIG. 38B

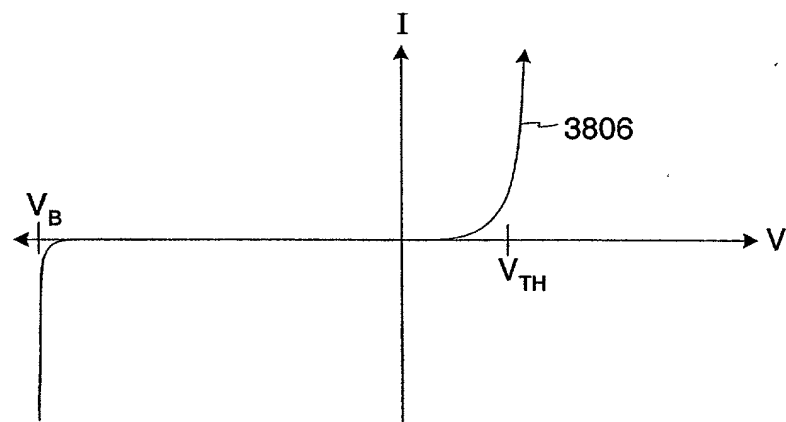


FIG. 38C

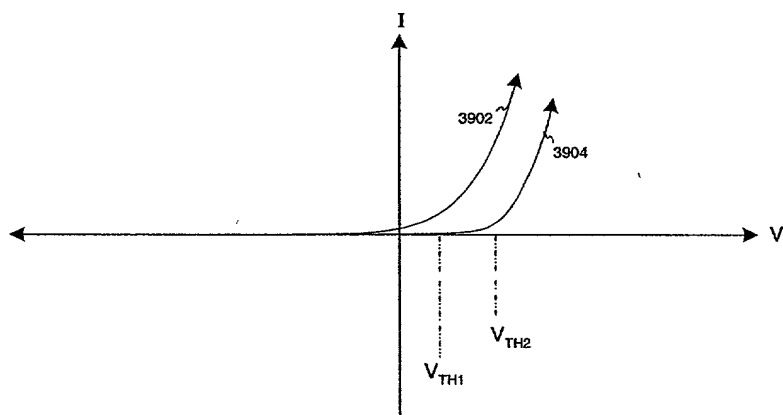


FIG. 39

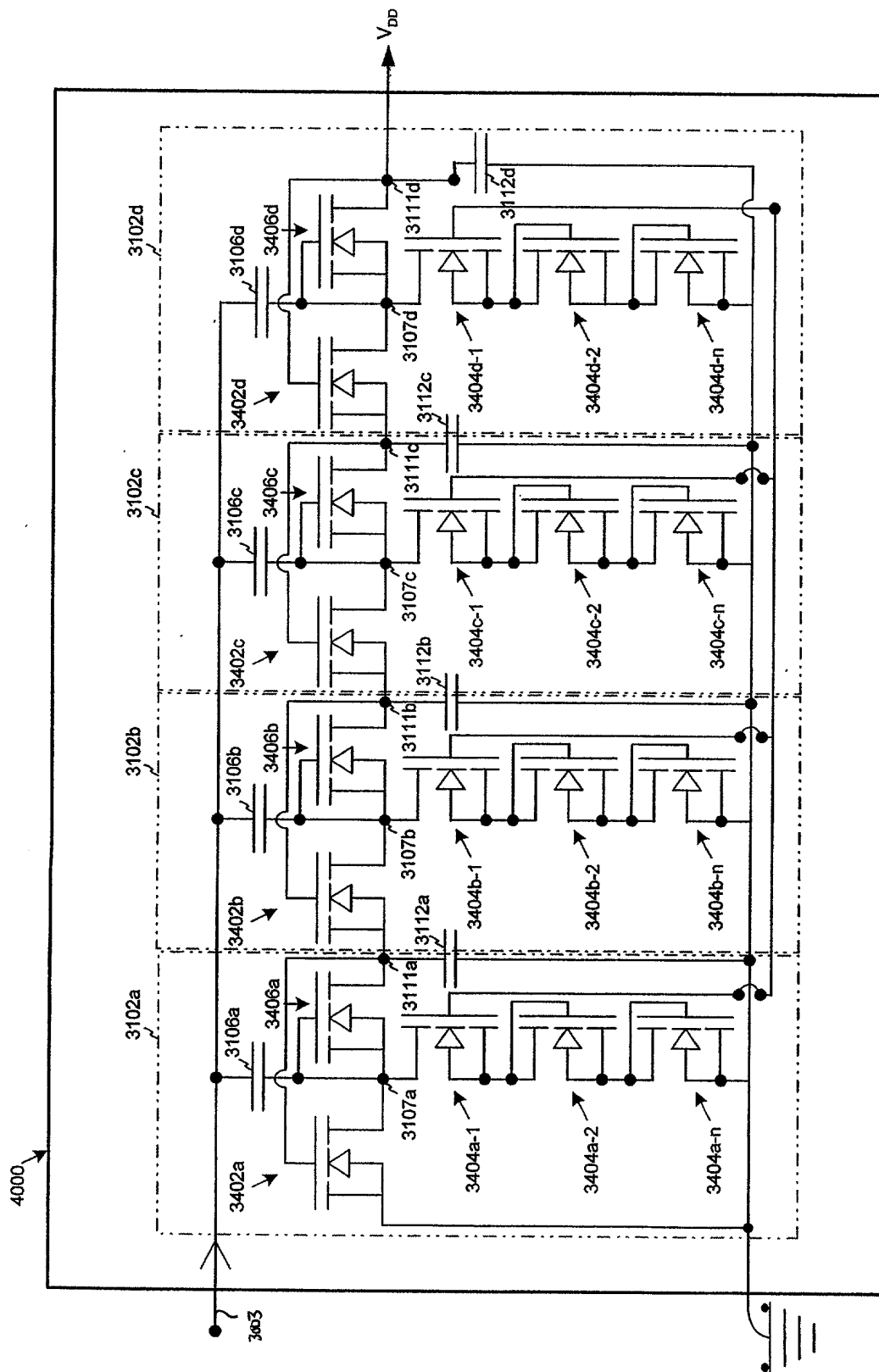


FIG. 40B

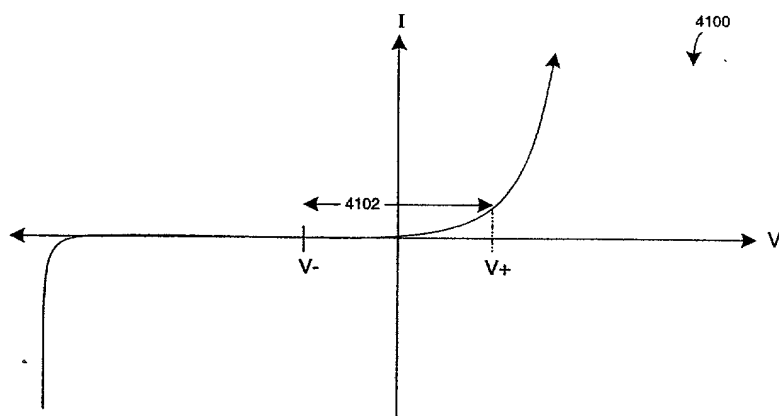


FIG. 41A

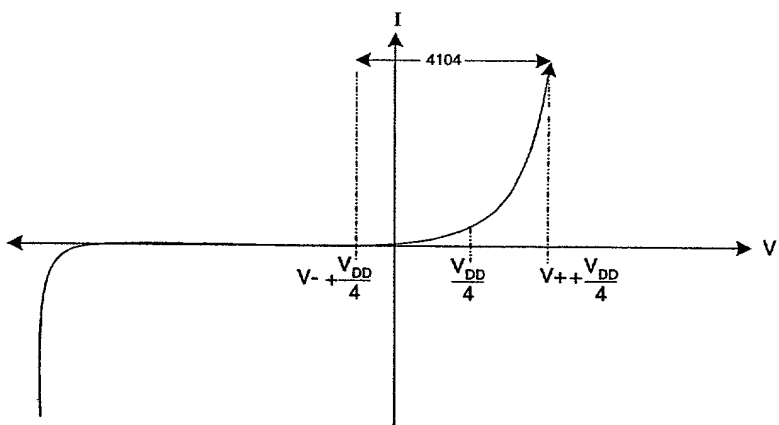


FIG. 41B

2025-04-08 10:00:00

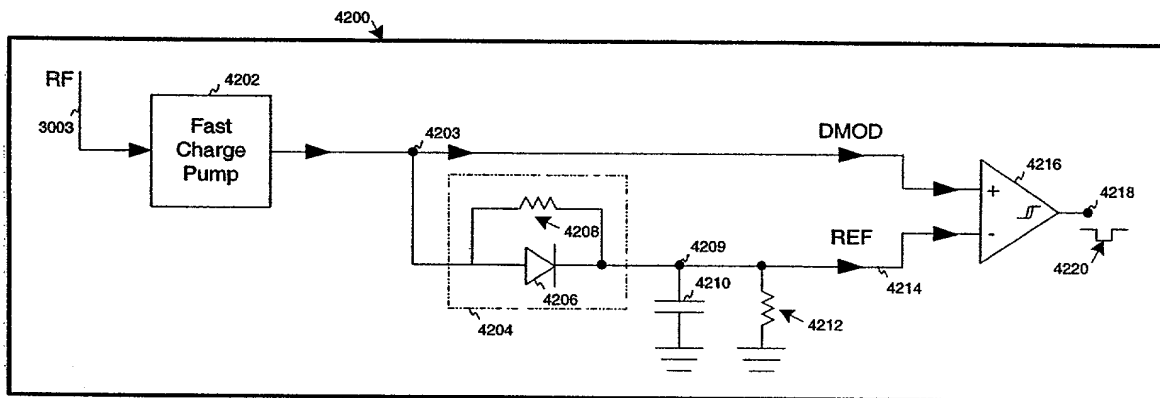


FIG. 42

2025-07-07 10:07:29

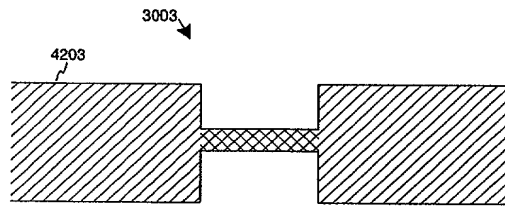


FIG. 43A

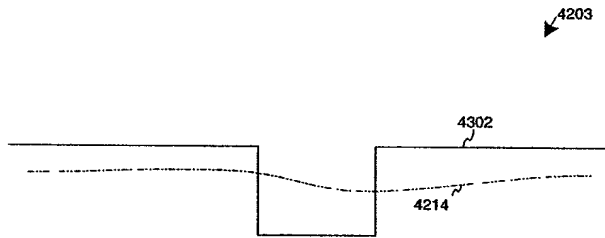


FIG. 43B

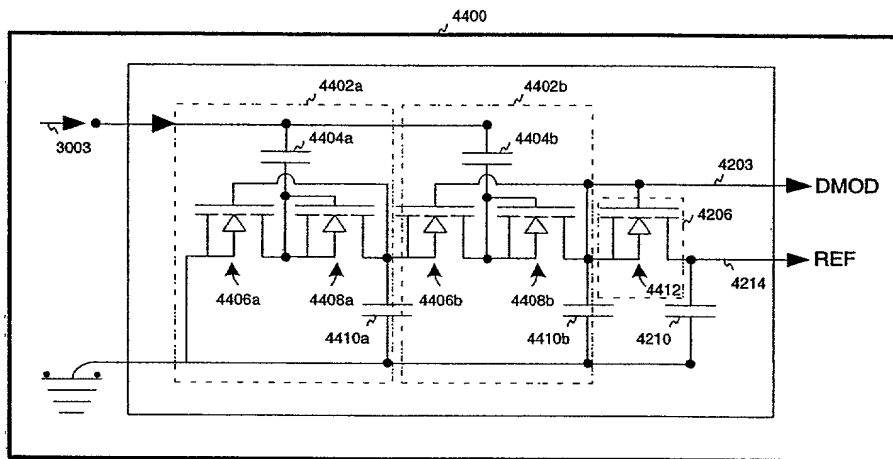


FIG. 44

2007-09-20 10:52:00

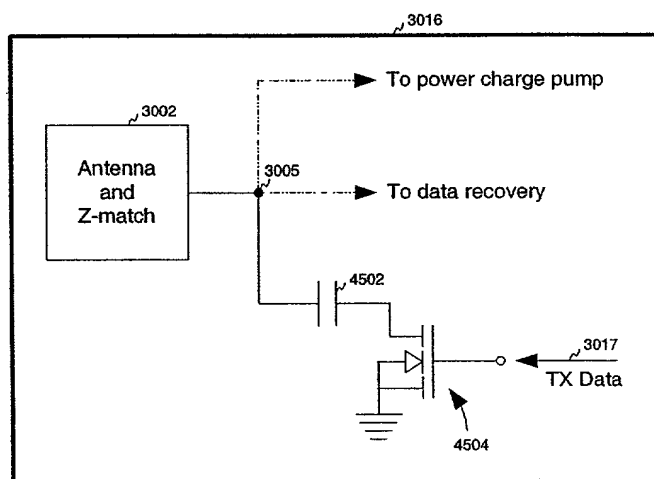


FIG. 45